Chapter 2 System Overview

2.1 Address Mapping

RKaudi support to boot from internal bootrom, which support remap function by software programming. Remap is controlled by GRF_SOC_CON0[12].

Reserved MED: A/M Reserved	Addr	IP	Addr	IP	Addr	IP	Addr	IP
Reserved 20038000 16K 10504000 20034000 16K Reserved 20034000 16K 10500000 16K 20090000 16K Reserved 2003000 16K 1040000 1024K 20082000 16K Reserved 2003000 6K 1040000 1024K 20082000 16K Reserved 2003000 6K 1030000 1024K 20082000 16K Neserved 2003000 6K 102000 784K 20084000 16K Neserved D06, PTY AHB ARB1 20084000 16K 20084000 16K 1013600 16K 2009000 24K 10224000 32K 20084000 16K 10138000 10K 2009000 6K 10224000 6KK 2007000 16K 10138000 10K 10224000 16K 2007000 16K 2007000 16K 10138000 32K 20094000 16K 20074000 16K		Reserved		MIPI-ANA		Reserved		Reserved
Reserved 20030000 10XH - AKA NANDC eFuse eFuse 2003000 16K 1050000 10K 2009000 16K Reserved 0030000 16K 1040000 1024K 2008000 16K 1013e00 20030000 64K 10330000 1024K 2008000 16K 1013e00 4K 2001000 64K 1023000 1024K 2008000 16K 1013e00 4K 2001000 64K 1023000 784K 2008000 16K 1013e00 16K 20008000 5K 1022400 64K 2007000 16K 1013e00 16K 20008000 5K 1022400 64K 2007000 16K 1013e000 32K 20000000 16K 1022400 64K 2007000 16K 1013e000 32K 20000000 16K 1021000 16K 2007000 16K 1013e000 32K 20000000 16K 2007000 <th></th> <th></th> <th>20038000</th> <th>16K</th> <th>10504000</th> <th></th> <th>20094000</th> <th></th>			20038000	16K	10504000		20094000	
Reserved ACODEC-ANA ACODEC-ANA DISCONC 1050000 16K 2009000 16K Reserved DBC 1040000 1024K 2008000 16K Reserved 2002000 64K 1030000 1024K 2008000 16K Reserved 2002000 64K 1030000 1024K 2008000 16K Reserved 2002000 64K 1023000 784K 2008400 16K I0134000 4K 2001000 64K 1023000 32K 2008400 16K Reserved DDR_PHY AHB ARB0 GP101 1013000 32K 2008000 16K 10136000 16K 2007000 16K 1022400 64K 20072000 16K 1013000 32X 20004000 16K 1022000 16K 20078000 16K 1013000 32X 20000000 16K 1021000 16K 20078000 16K 1013000 32K 20000000 16K 102		Reserved		HDMI-ANA		NANDC		eFuse
Reserved ACODEC ANA GPS GMAC 101 30000 108 1040000 1024K 2008000 16K 101 30000 200 20000 64K 1030000 1024K 2008000 16K 101 30000 4K 200 10000 64K 1023000 784K 2008000 16K 101 30000 4K 200 10000 64K 1023000 784K 2008000 16K 101 30000 16K 200 08000 20K 1023400 32K 2008000 16K 101 30000 16K 200 08000 3K 10224000 64K 2007000 16K 101 30000 22K 200 08000 16K 1022000 16K 2007000 16K 101 30000 22K 200 09000 16K 1022000 16K 2007000 16K 101 30000 32K 200 09000 16K 1021000 16K 2007000 16K 101 2000 32K 200 09000 16K 2007000			20034000	16K	10500000	16K	20090000	16K
Reserved 104.0000 102.4K 2008.000 16K Reserved 000 102.4K 2008.000 16K 1013e000 4K 200.0000 64K 103.0000 102.4K 200.8000 16K Reserved Reserved AHB ARB1 GP103 GP101 1013e000 4K 200.9000 24K 102.3400 32K 200.8000 16K Reserved DDR_PHY AHB ARB0 200.8000 16K 200.8000 16K 1013e000 16K 200.9000 8K 102.24000 64K 200.7000 16K 1013e000 16K 200.9000 8K 102.2000 64K 200.7000 16K 1013e000 32K 200.9000 16K 102.1000 16K 200.7000 16K 1012e000 32K 200.9000 16K 102.1000 16K 200.7000 16K 1012e000 32K 200.9000 16K 102.1000 16K 200.7000 16		Reserved		ACODEC-ANA		GPS		GMAC
Reserved DBC PFR 1915 GP103 1013e000 Reserved 1030000 1024k 2008000 16K 1013d000 4K 2001000 64K 1023000 784K 2008400 16K 1013d000 4K 2000a000 24K 1023000 784K 2008000 16K 1013c000 8K 2000a000 24K 10234000 32K 2008000 16K 1013000 10K 2000a000 24K 10224000 64K 2007000 16K 1013000 10K 2000000 8K 10224000 16K 2007000 16K 10130000 32K 20004000 16K 1022000 16K 20074000 16K 10128000 32K 20004000 16K 10218000 16K 20074000 16K 1018000 64K 10214000 16K 2007000 16K 10112000 16K 10214000 16K 20060000 16K			20030000	16K	10400000	1024K	2008C000	16K
1013e000 20220000 64K 1030000 1024k 20088000 16K 013d000 4K 20010000 64K 1023000 784K 20088000 16K 013c000 8k 2000a000 24K 1023000 32K 2008000 16K 013c000 8k 2000a000 24K 1023000 32K 2008000 16K 0133000 10K 2000a000 8K 10224000 64K 2007C000 16K 0130000 32K 20004000 8K 1022000 64K 20072000 16K 0130000 32K 20004000 10K 1022000 16K 20074000 16K 0130000 32K 20004000 10K 1021000 16K 20074000 16K 0130000 32K 20004000 10K 1021000 16K 2007000 16K 0130000 64K 1021000 16K 2007000 16K 2007000 16K <td< th=""><th></th><th>Reserved</th><th></th><th>DBG</th><th></th><th>PERI BUS</th><th></th><th>GPIO3</th></td<>		Reserved		DBG		PERI BUS		GPIO3
Reserved Reserved AHB ARB1 GPI02 1013d000 4K 20010000 68K 1023C000 784K 20084000 16K 1013c000 8k 2000s000 24K 10234000 32K 20084000 16K 10138000 16K 2000s000 24K 10224000 64K 2007C000 16K 10138000 32X 20004000 16K 1022000 64K 2007C000 16K 10138000 32X 20004000 16K 1022000 16K 2007000 16K 10128000 32X 20004000 16K 10221000 16K 2007000 16K 10128000 32X 20004000 16K 1021000 16K 2007000 16K 10114000 16K 10214000 16K 2007000 16K 10112000 8K 1020400 32k 20064000 16K 10112000 8K 1020400 16k 20064000 16K <tr< th=""><th>1013e000</th><th></th><th>200 20000</th><th>64K</th><th>10300000</th><th>1024k</th><th>20088000</th><th>16K</th></tr<>	1013e000		200 20000	64K	10300000	1024k	20088000	16K
1013d000 4K 20010000 64K 1023C000 784K 20084000 16K 1013c000 8K 2000a000 24K 10234000 32K 2008000 16K 10138000 16K 2008000 8K 10224000 64K 2007C000 16K 10138000 18K 20008000 8K 10224000 16K 20077000 16K 10138000 32K 20004000 16K 10220000 16K 20078000 16K 10128000 32K 20004000 16K 1021000 16K 20074000 16K 1018000 64X 64X 10218000 16K 20074000 16K 1014000 16k 10218000 16K 20007000 16K 10114000 8K 1020000 16K 2006000 16K 10114000 8K 1020000 16K 2006000 16K 10114000 8K 1020000 16K 2006000 16K <th></th> <th>Reserved</th> <th></th> <th>Reserved</th> <th></th> <th>AHB ARB1</th> <th></th> <th>GPIO2</th>		Reserved		Reserved		AHB ARB1		GPIO2
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CIC CGF Reserved GPID 2007000 8K 10224000 64K 2007000 16K Normannia 2008000 8K 10224000 16K 2007000 16K 1013000 32K 2004000 18K 1022000 16K 2007000 16K 10128000 32K 20004000 18K 1021000 16K 20074000 16K CPU BUS 20004000 16K 10218000 16K 20074000 16K CPU BUS 20004000 16K 10218000 16K 20074000 16K Reserved 0 10218000 16K 20074000 16K 20074000 16K 10114000 64K 0 10214000 16K 2006000 16K MBPctH 0 10214000 16K 2005000 16K 10112000 8K 0 10204000 16K 2006000 16K 10112000 8K 0 10204000 16K </th <th>1013c000</th> <th>8k</th> <th>2000a000</th> <th>24K</th> <th>10234000</th> <th>32K</th> <th>20080000</th> <th>16K</th>	1013c000	8k	2000a000	24K	10234000	32K	20080000	16K
10138000 16K 20008000 8K 10224000 64K 2007C000 16K Reserved DR_PCIL III IIII IIIII DR_PCIL IIIIIIIII DMAC 1013000 32K 20004000 16K IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII		GIC		GRF		Reserved		GPI0
Reserved DDR_PCTL I2S_2ch DMAC 10130000 32K 20004000 10K 1022000 16K 20078000 16K CPU BUS CRU eMMC 20078000 16K 20078000 16K 10128000 32K 20000000 16K 1021C00 16K 20074000 16K Reserved CRU 1021C00 16K 20074000 16K 2007000 16K Reserved C SDIO 2007000 16K 2007000 16K 10118000 64K C C SDIMC 2006000 16K B6A C 10214000 16K 2006000 16K 10114000 BK C 1020000 32k 20068000 16K I0114000 BK C 1020000 16k 2006000 16K I0114000 BK C I0204000 16k 2006000 16K I0110000 BK C I0204000<	10138000	16K	20008000	8K	10224000	64K	2007C000	16K
101 30000 32K 20004000 16K 10220000 16K 20078000 16K CPU BUS CRU eMMC SPI SPI SPI 101 28000 32K 2000000 16K 1021000 16K 20074000 16K Reserved 10212000 16K 20074000 16K 20074000 16K 10118000 64K 10218000 16K 20070000 16K Beserved 10214000 16K 2007000 16K I0114000 15K 10214000 16K 2006000 16K Beserved 1020000 32k 20068000 16K MIP1_ctrl 10208000 16k 20064000 16K CD000 3K 10204000 16k 2005000 16K 010ec00 3K 10204000 16k 2005000 16K 010ec00 3K 101E000 128K 20058000 16K 0101e000 3K 101E000 <		Reserved		DDR_PCTL		I2S_2ch		DMAC
CPU BUSCRUeMMCSPI1012800032K2000000016K102100016K2007400016KReserved01021800016K2007000016K1011800064K1021800016K2007000016KEBC01021400016K2005000016K1011400016k2005000016K2005000016KReserved01021400016K2005000016K101120008K1020c00032k2005800016K101100008K01020800016K2005000016K1010e0008K01020400016k2005000016K1010e0008K01020400016k2005000016K1010e0008K0102000016k2005000016K1010e0008K0102000016k2005000016K1010e0008K01012000016K2005000016K1010e0008K001018000128K2005800016K101000018K001018000256K2005000016K1010000018K00016K200400016K1010000018K000200400016K1010000018K00016K200400016K1010000018K000016K1010000018K	10130000	32K	20004000	16K	10220000	16K	20078000	16K
10128000 32K 2000000 16K 1021C000 16K 20074000 16K Reserved 0 0118000 16K SDIO 1021 102000 16K 20070000 16K EBC 0 10218000 16K 20070000 16K 10114000 16k 10214000 16K 20070000 16K 10112000 8K 0 10214000 16K 2006000 16K 10112000 8K 0 1020c000 32k 20068000 16K 10110000 8K 0 10208000 16k 20064000 16K 1010000 8K 0 10208000 16k 20064000 16K 1010e000 8K 0 10204000 16k 2005000 16K 1010e000 8K 0 10204000 16k 2005000 16K 10108000 8K 0 10120000 16K 2005000 16K 10108000		CPU BUS		CRU		eMMC		SPI
Reserved SDIO J2C0 10118000 64K 10218000 16K 20070000 16K EBC 10214000 16K 2006000 16K 10114000 10k 10214000 16K 2006000 16K Reserved 10214000 16K 2006000 16K 10112000 8K 1020c000 32k 20068000 16K MIP1_ctrl 1020000 16k 20064000 16K LCDC0 SPDIF UART0 UART0 1010e000 8K 10204000 16k 20060000 16K RCA 10204000 16k 2005000 16K 12C3 1010e000 8K 10204000 16k 2005000 16K CLF USB HOST OHCI 12C3 101C3000 16K 20054000 16K 10168000 8K 101C0000 128K 20054000 16K 10108000 10K 10180000 256K 2004000	101 28000	32K	200 00000	16K	1021C000	16K	20074000	16K
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EBC SDMMC SARADC 10114000 16k 2006C000 16K Reserved 020000 32k 2006C000 16K MIP1_ctrl 1020000 32k 2006000 16K MIP1_ctrl 1020000 32k 2006000 16K LCDC0 TSP UART1 1010000 8K 10208000 16k 2006000 16K LCDC0 SPDIF UART0 UART0 UART0 1010000 8K 10208000 16k 2005000 16K CLCDC0 SRGA 10208000 16k 2005000 16K 0100000 8K 1020000 16k 2005000 16K CLF USB HOST OFLI I2C1 I2C2 I2C1	10118000	64K			10218000	16K	20070000	16K
10114000 16k 2006C000 16K Reserved 10214000 16K 2006C000 16K I0112000 8K 1020c00 32k 20068000 16K MIP1_ctrl 1020800 16k 20064000 16K I010000 8K 1020800 16k 20064000 16K I010e000 8K 10204000 16k 2006000 16K I010e000 8K 1020000 16k 2005000 16K I010e000 8K 1020000 16k 2005000 16K I010e000 8K 1020000 16k 2005000 16K I010e000 8K 101E000 128K 20054000 16K I010e000 8K 101E000 128K 20054000 16K I010e000 8K 101E000 128K 20054000 16K I010900 8K 101E000 128K 20054000 16K I010000 16K 1018000		EBC				SDMMC		SARADC
Reserved SFC UART2 10112000 8K 1020c000 32k 20068000 16K MIPI_ctri 10208000 16k 20064000 16K LCDC0 8K 10204000 16k 2006000 16K LCDC0 8K 10204000 16k 2006000 16K 1010e000 8K 10204000 16k 2005000 16K 1010e000 8K 1020000 16k 2005000 16K 1010e000 8K 1020000 16k 2005000 16K CIF USB HOST OHCI I2C2 1016000 128K 20058000 16K 10102000 8K 10110000 128K 20054000 16K VCODEC USB HOST ECHI I2C1 20054000 16K 1010000 16K 10180000 256K 20050000 16K 10100000 16K 2004000 16K WDT 2004000 16K 1006c000 <t< th=""><th>10114000</th><th>16k</th><th></th><th></th><th>10214000</th><th>16K</th><th>2006C000</th><th>16K</th></t<>	10114000	16k			10214000	16K	2006C000	16K
10112000 BK 1020c000 32k 20068000 16K MIPI_ctrl I0208000 I6k 20064000 I6K LCDC0 I0208000 I6k 20064000 I6K LCDC0 I0204000 I6k 20060000 I6K BGA I0204000 I6k 20060000 I6K RGA I0204000 I6k 20060000 I6K I010e000 BK I020000 I6k 2005000 I6K I010000 BK I020000 I6k 2005000 I6K I010a000 BK I01E000 I28K 20058000 I6K I0108000 BK I01C0000 I28K 20054000 I6K VCODEC USB HOST ECHI I2C1 I2C1 I018000 I6K 20054000 I6K I0104000 I6K I0180000 256K 2005000 I6K I0100000 I6K I0180000 2004000 I6K I0100000 I6K		Reserved				SFC		UART2
MIPI_ctrl TSP UART1 101 10000 8K 102 08000 16k 200 64000 16K LCDC0 101 0e000 8K 102 04000 16k 200 60000 16K RGA 102 00000 16k 200 60000 16K 200 60000 16K 01 0e 000 8K 102 00000 16k 200 50000 16K CIF USB HOST OHCI 12C 2 200 58000 16K 200 58000 16K I01 0e 000 8K 101 C0000 128K 200 58000 16K 200 58000 16K I01 0e 000 8K 101 C0000 128K 200 58000 16K 200 58000 16K I01 0e 000 8K 101 C0000 128K 200 50000 16K 200 50000 16K 200 50000 16K I01 0e 000 16K 101 80000 256K 200 50000 16K 200 50000 16K I01 0e 000 16K 101 80000 256K 200 50000 16K	10112000	вк			1020c000	32k	20068000	16K
10110000 3K 10208000 16k 20064000 16K LCDC0 0 <t< th=""><th></th><th>MIPI_ctrl</th><th></th><th></th><th></th><th>TSP</th><th></th><th>UART1</th></t<>		MIPI_ctrl				TSP		UART1
LCDC0 SPDIF UARTO 1010e000 8K 10204000 16k 20060000 16K RGA 1020000 16k 2005000 16K I2C3 1010c000 8K 1020000 16k 2005000 16K CIF USB HOST OHCI I2C2 I2C1 I2C2 1010a000 8K 101E0000 128K 20058000 16K IEP USB HOST ECHI I2C1 I2C1 I2C1 I2C1 10108000 8K 101E0000 128K 20054000 16K VCODEC USB OTG PWM0 I0180000 256K 20050000 16K NOM I0180000 256K 20050000 16K WDT I0108000 16K WDT 10100000 16K I I III III III III IIII IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	10110000	8K			10208000	16k	20064000	16K
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CIF USB HOST OHCI I2C2 1010a000 8K 101E0000 128K 20058000 16K IEP USB HOST ECHI USB HOST ECHI I2C1 I2C1 10108000 8K 101C0000 128K 20054000 16K VCODEC USB OTG PWM0 10180000 256K 20050000 16K ROM 10180000 256K 20050000 16K WDT 10100000 16K 10180000 256K 2004000 16K ROM IOI IOI IOI III IIII IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	1010c000	8к			10200000	16k	2005C000	16K
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IEP USB HOST ECHI I2C1 10108000 8K 101 C0000 128K 20054000 16K VCODEC USB OTG USB OTG PWM0 10104000 16K 10180000 256K 20050000 16K ROM 10180000 256K 20050000 16K ROM 10180000 256K 2004000 16K I006000 16K Intervention Intervention Intervention Intervention 1006000 16K Intervention Intervention Intervention Intervention Intervention 1006000 16K Intervention Intervention Intervention Intervention Intervention I006000 304K Intervention Intervention Intervention Intervention Intervention I006000 304K Intervention Intervention Intervention Intervention	1010a000	8к			101E0000	128K	20058000	16K
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10104000 16K 10180000 256K 20050000 16K ROM Intervention Interventin Intervention		VCODEC				USB OTG	20001000	PWM0
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crypto SCR 100fc000 16K Reserved 20048000 100b000 304K PMU	10100000	16K					20040000	16K
100fc000 16K 20048000 16K Reserved 20048000 16K 100b0000 304K 20044000 16K PMU		crynto					20040000	SCR
Reserved Image: Constraint of the constraint	100fc000	16K					20048000	16K
100b0000 304K 20044000 16K		Reserved					20040000	TIMER0-5
	10060000	304K					20044000	16K
	1000000	PMI					20044000	100
10030000 64K	100-20000	64K						
	2000000	CPU						
	10000000	644						
	10090000	UTK		hafaya		after v		
				Derore remap		aiter remap		
				10.00	10080000/			
10080000 8k 0000000 8k			10080000	8k	00000000	8k		



2.2 System Boot

RKaudi provides system boot from off-chip devices such as SDMMC card, 8bits async nand flash or toggle nand flash, SPI nor or nand, and eMMC memory.

When boot code is not ready in these devices, also provide system code download into them by USB OTG interface. All of the boot code will be stored in internal bootrom. The following is the whole boot procedure for boot code, which will be stored in bootrom in advance.

The following features are supports.

- Support secure boot mode and non-secure boot mode
- Support system boot from the following device:
- 8bits Async Nand Flash
- 8bits Toggle Nand Flash
- SFC interface
- eMMC interface
- SDMMC Card
 - Support system code download by USB OTG





2.3 System Interrupt connection

RKaudi provides an general interrupt controller(GIC) for Cortex-A7 MPCore processor, which has 112 SPI (shared peripheral interrupts) interrupt sources and 3 PPI(Private peripheral interrupt) interrupt source and separately generates one nIRQ and one nFIQ to CPU. The triggered type for each interrupts is high level sensitive, not programmable. The detailed interrupt sources connection is in the following table. For detailed GIC setting, please refer to

Chapter 12.

Table 2-1 RKaudi Interrupt connection list

	IRQ ID	Source	Polarity	
	27	VIRTUAL TIMER	High level	
PPI	29	SECURE PHYSICAL TIMER	High level	
	30	NON-SECURE PHY TIMER	High level	
	32	DMAC2(0)	High level	
	33	DMAC2(1)	High level	
	34	DDR_PCTL	High level	
	35	gpu_irqgp	High level	
	36	gpu_irqmmu	High level	
	37	gpu_irqpp	High level	
	38	Video encoder	High level	
	39	Video decoder	High level	
	40	CIF	High level	
	41	LCDC	High level	
	42	USB OTG	High level	
	43	USB Host EHCI	High level	
	44	gps_irq	High level	
	45	gps_timer_irq	High level	
	46	SD/MMC0	High level	
	47	SDIO	High level	
	48	eMMC	High level	
	49	SAR-ADC	High level	
Source(spi)	50	NandC	High level	
	51	I2S_2ch	High level	
	52	UART0	High level	
	53	UART1	High level	
	54	UART2	High level	
C	55	SPI0	High level	
	56	I2C0	High level	
$\Delta \mathbf{O}$	57	I2C1	High level	
\sim	58	I2C2	High level	
	59	I2C3	High level	
	60	Timer0	High level	
	61	Timer1	High level	
	62	PWM	High level	
	63	PMU	High level	
	64	USB Host OHCI	High level	
	65	MIPI_controller	High level	
	66	WDT	High level	
	67	otg_bvalid_irq	High level	
	68	GPIO0	High level	

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69GPI01High level70GPI02High level71GPI03High level72CRYPTOHigh level73reservedHigh level74peri_ahb_usb arbiterHigh level75reservedHigh level76RGAHigh level77hdmiHigh level78SD/MMC detectHigh level80IEPHigh level81EBCHigh level83otg0_id_irqHigh level84otg0_linestate_irqHigh level	
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79SDIO detectHigh level80IEPHigh level81EBCHigh level82sfcHigh level83otg0_id_irqHigh level84otg0_linestate_irqHigh level85otg1 linestate irgHigh level	
80IEPHigh level81EBCHigh level82sfcHigh level83otg0_id_irqHigh level84otg0_linestate_irqHigh level85otg1 linestate irgHigh level	
81 EBC High level 82 sfc High level 83 otg0_id_irq High level 84 otg0_linestate_irq High level 85 otg1_linestate_irq High level	
82 sfc High level 83 otg0_id_irq High level 84 otg0_linestate_irq High level 85 otg1_linestate_irq High level	
83 otg0_id_irq High level 84 otg0_linestate_irq High level 85 otg1_linestate_irg High level	
84 otg0_linestate_irq High level 85 otg1_linestate_irg High level	,
85 lota1 linestate ira lHigh level	
86 sd_detectn_irq High level	
87 spdif High level	
88 gmac High level	
89 gmac_tmc High level	
90 tsp High level	
91 timer2 High level	
92 timer3 High level	
93 timer4 High level	
94 timer5 High level	
95 sim_card High level	
96 acodec_detectn_irq High level	
97 hevc_mmu_irq High level	
98 hevc_dec_irq High level	
99 vpu_mmu_irq High level	
100 i2s_8ch High level	
101 Reserved High level	
102 Reserved High level	
103 Reserved High level	
104 Reserved High level	
105 Reserved High level	
106 Reserved High level	
107 Reserved High level	
108 pmuirq_a7_0 High level	
109 pmuirq_a7_1 High level	
110 pmuirq_a7_2 High level	1

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111	pmuirq_a7_3	High level
112	axierrirq	High level

2.4 System DMA hardware request connection

RKaudi provides one DMA controller: DMAC inside peripheral system. 15 hardware request ports are used in DMAC_PERI, the trigger type for each of them is high level, not programmable. For detailed descriptions of DMAC, please refer to Chapter 11.

Req Number	Source	Polarity
0	I2S_2ch tx	High level
1	I2S_2ch rx	High level
2	Uart0 tx	High level
3	Uart0 rx	High level
4	Uart1 tx	High level
5	Uart1 rx	High level
6	Uart2 tx	High level
7	Uart2 rx	High level
8	SPI tx	High level
9	SPI rx	High level
10	SD/MMC	High level
11	SDIO	High level
12	еММС	High level
13	SPDIF	High level
14	I2S_8ch tx	High level
15	I2S_8ch rx	High level

Table 2-2 RKaudi DMAC_BUS Hardware request connection list

Bock