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6 LAYERS PCB STACK

<table>
<thead>
<tr>
<th>Layer</th>
<th>Material</th>
<th>Thickness</th>
<th>Dielectric Constant</th>
</tr>
</thead>
<tbody>
<tr>
<td>TOP</td>
<td>Cu, thickness: 0.7mil, Plating to 1oz</td>
<td>FR4, thickness: 3.8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
<tr>
<td>GND1</td>
<td>Cu, thickness: 1.5mil, 1oz</td>
<td>FR4, thickness: 3.8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
<tr>
<td>POWER</td>
<td>Cu, thickness: 1.5mil, 1oz</td>
<td>FR4, thickness: 3.8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
<tr>
<td>Signal2</td>
<td>Cu, thickness: adjust thickness according to the thickness of board, Dielectric Constant: 4.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>GS2</td>
<td>Cu, thickness: 1.5mil, 1oz</td>
<td>FR4, thickness: 3.8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
<tr>
<td>BOTTOM</td>
<td>Cu, thickness: 0.7mil, Plating to 1oz</td>
<td>FR4, thickness: 8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Cu, thickness: 1.5mil, 1oz</td>
<td>FR4, thickness: 8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Cu, thickness: 1.5mil, 1oz</td>
<td>FR4, thickness: 8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Cu, thickness: 1.5mil, 1oz</td>
<td>FR4, thickness: 8mil, Dielectric Constant: 4.3</td>
<td></td>
</tr>
</tbody>
</table>

6 LAYERS PCB STACK
If you use MCU_JL8506 - double code - V6.04. Need to use external reset IC. R203 don't stick.
Note:
These termination resistors must be placed in the middle of trace, and the termination resistor of CLK must be placed in the bifurcation point.
If the connection to the international standard, R262 R263 don’t pick up
If the connection is millet, R201 R264 don’t pick up
Optical S/PDIF OUT

WORKING_LED

ATTINY13A
If use external clock then the XTAL2 need connect to GND for RTL8211E.

PHY Address=001(RTL8211E)
Config for all capability
Without RX Delay
Without TX Delay
Connect ENSWREG to AVDD33 to enable Switching regulator or connect ENSWREG to GND to disable Switching regulator.

Inductance close to PIN48
Close to PHY
RK3288_H

Title: RK3288 SDIO Controller

Date: Thursday, June 19, 2014

Page: 19

Revision: R3.0-1

File: Firefly

Modify Date: Wednesday, June 18, 2014
Note:
Adjusted the load capacitance according to the crystal specification.

AP6234/ AP6335: 37.4MHz
Other: 26MHz

Note: Yes: Inside the box to
No: The box is not posted