

High Speed LDO Regulators, High PSRR, Low noise, PT5108 Series

General Description

The PT5108 series are highly accurate, low noise, CMOS LDO Voltage Regulators. Offering low output noise, high ripple rejection ratio, low dropout and very fast turn-on times, the PT5108 series is ideal for today's cutting edge mobile phone. Internally the PT5108 includes a reference voltage source, error amplifiers, driver transistors, current limiters and phase compensators. The PT5108's current limiters' foldback circuit also operates as a short protect for the output current limiter and the output pin. The PT5108 series is also fully compatible with low ESR ceramic capacitors, reducing cost and improving output stability. This high level of output stability is maintained even during frequent load fluctuations, due to the excellent transient response performance and high PSRR achieved across a broad range of frequencies. The CE function allows the output of regulator to be turned off, resulting in greatly reduced power consumption.

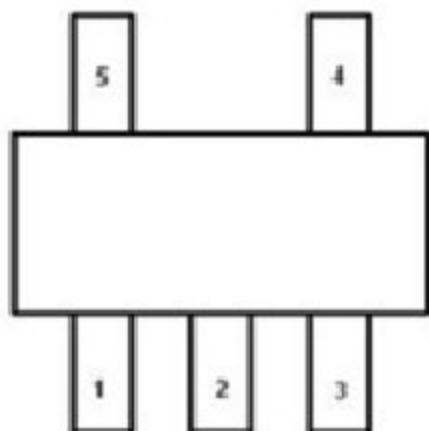
Features

- Maximum Output Current: 500mA ($V_{IN}=4.3V, V_{OUT}=3.3V$)
- Dropout Voltage: 100mV@ $I_{OUT} = 100mA$
- Operating Voltage Range: 2V~6.0V
- Highly Accuracy: $\pm 2\%$
- Low Power Consumption: 50uA (TYP.)
- Standby Current: 0.1uA (TPY.)
- High Ripple Rejection: 70dB@1KHz (PT5108E23E-33)
- Low output noise: 50uVrms
- Line Regulation: 0.05% (TYP.)
- Ultra Small Packages: SOT-23-5

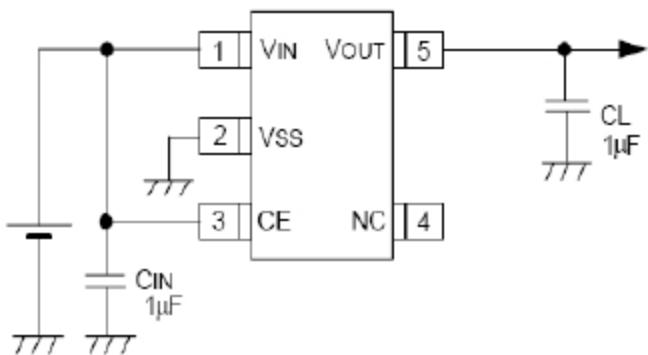
Typical Application

- Mobile phones
- Cordless phones, radio communication equipment
- Portable games
- Cameras, Video cameras
- Reference voltage sources
- Battery powered equipment

Pin Configuration



Typical Application Circuit



Pin Assignment

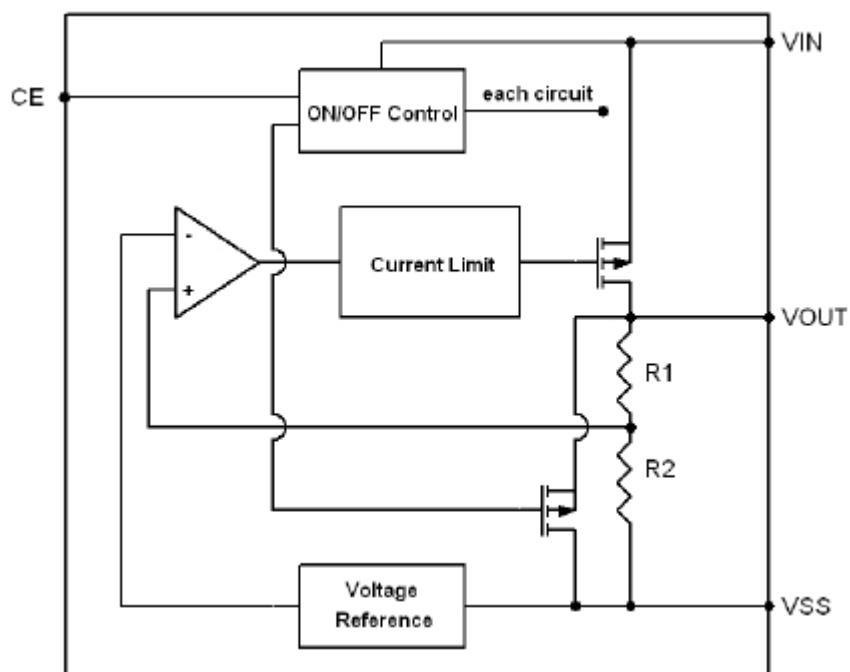
PT5108

Pin Number	Pin Name	Functions
SOT-23-5		
1	V _{IN}	Power Input
2	V _{SS}	Ground
3	CE	ON / OFF Control
4	NC	No Connect
5	V _{OUT}	Output

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Units
Input Voltage	V _{IN}	6.5	V
Output Current	I _{OUT}	600	mA
Output Voltage	V _{OUT}	V _{SS} -0.3~V _{IN} +0.3	V
CE Pin Voltage	V _{CE}	V _{SS} -0.3~V _{IN} +0.3	V
Power Dissipation	P _D	250 250 300 500	mW
Operating Temperature Range	T _{OPR}	-40~+85	°C
Storage Temperature Range	T _{STG}	-40~+125	°C

Block Diagram



Electrical Characteristics
PT5108E23E-12
 $(V_{IN} = V_{OUT} + 1V, \ V_{CE} = V_{IN}, \ C_{IN}=C_L=1\mu F, \ Ta=25^{\circ}C)$, unless otherwise noted

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Output Voltage	$V_{OUT}(E)$ (Note 2)	$I_{OUT}=30mA,$ $V_{IN}=V_{OUT}+1V$	X 0.98	$V_{OUT}(T)$ (Note 1)	X 1.02	V
Maximum Output Current	I_{OUTMAX}	$V_{IN}=V_{OUT}+1V$		300		mA
Load Regulation	ΔV_{OUT}	$V_{IN}=V_{OUT}+1V, \ 1mA \leq I_{OUT} \leq 100mA$		8		mV
Dropout Voltage (Note 1)	V_{DIF1}	$I_{OUT} = 100mA$		280		mV
	V_{DIF2}	$I_{OUT} = 200mA$		500		mV
Supply Current	I_{SS}	$V_{IN}=V_{OUT}+1V$		40		μA
Stand-by Current	I_{CEL}	$V_{CE}=0V$		0.1		μA
Line Regulation	ΔV_{OUT} $\Delta V_{IN} \cdot V_{OUT}$	$I_{OUT} = 40mA$ $V_{OUT}+1V \leq V_{IN} \leq 6.5V$		0.03		%/V
CE "High" Voltage	V_{CEH}	Start up	1.0			V
CE "Low" Voltage	V_{CEL}	Shut down			0.7	V
Output noise	EN	$I_{OUT} = 40mA, \ 300Hz \sim 50kHz$		50		μV_{rms}
Ripple Rejection Rate	PSRR	$V_{IN} = [V_{OUT} + 1]V$	$I_{OUT}=10mA, 1kHz$	70		dB
		$+1V_{p-pAC}$	$I_{OUT}=100mA, 10kHz$	62		

PT5108X23E-18
 $(V_{IN} = V_{OUT} + 1V, V_{CE} = V_{IN}, C_{IN} = C_L = 1\mu F, Ta = 25^\circ C)$, unless otherwise noted

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Units
Output Voltage	$V_{OUT}(E)$ (Note 2)	$I_{OUT} = 30mA, V_{IN} = V_{OUT} + 1V$		X 0.98	$V_{OUT}(T)$ (Note 1)	X 1.02	V
Maximum Output Current	I_{OUTMAX}	$V_{IN} = V_{OUT} + 1V$			300		mA
Load Regulation	ΔV_{OUT}	$V_{IN} = V_{OUT} + 1V, 1mA \leq I_{OUT} \leq 100mA$			9		mV
Dropout Voltage (Note 1)	V_{DIF1}	$I_{OUT} = 100mA$			200		mV
	V_{DIF2}	$I_{OUT} = 200mA$			400		mV
Supply Current	I_{SS}	$V_{IN} = V_{OUT} + 1V$			45		μA
Stand-by Current	I_{CEL}	$V_{CE} = 0V$			0.1		μA
Line Regulation	ΔV_{OUT} $\Delta V_{IN} \cdot V_{OUT}$	$I_{OUT} = 40mA$ $V_{OUT} + 1V \leq V_{IN} \leq 6.5V$			0.05		%/V
CE "High" Voltage	V_{CEH}	Start up		1.0			V
CE "Low" Voltage	V_{CEL}	Shut down				0.7	V
Output noise	EN	$I_{OUT} = 40mA, 300Hz \sim 50kHz$			50		μV_{rms}
Ripple Rejection Rate	PSRR	$V_{IN} =$ $[V_{OUT} + 1]V + 1V$ p-pAC	$I_{OUT} = 10mA, 1kHz$ $I_{OUT} = 100mA, 10kHz$		70 62		dB

PT5108E23E-28
 $(V_{IN} = V_{OUT} + 1V, V_{CE} = V_{IN}, C_{IN} = C_L = 1\mu F, Ta = 25^\circ C)$, unless otherwise noted

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Output Voltage	$V_{OUT}(E)$ (Note 2)	$I_{OUT} = 30mA, V_{IN} = V_{OUT} + 1V$	X 0.98	$V_{OUT}(T)$ (Note 1)	X 1.02	V
Maximum Output Current	I_{OUTMAX}	$V_{IN} = V_{OUT} + 1V$		450		mA
Load Regulation	ΔV_{OUT}	$V_{IN} = V_{OUT} + 1V, 1mA \leq I_{OUT} \leq 100mA$		7		mV
Dropout Voltage (Note 1)	V_{DIF1}	$I_{OUT} = 100mA$		110		mV
	V_{DIF2}	$I_{OUT} = 200mA$		220		mV
Supply Current	I_{SS}	$V_{IN} = V_{OUT} + 1V$		55		μA
Stand-by Current	I_{CEL}	$V_{CE} = 0V$		0		μA
Line Regulation	ΔV_{OUT} $\Delta V_{IN} \cdot V_{OUT}$	$I_{OUT} = 40mA$ $V_{OUT} + 1V \leq V_{IN} \leq 6.5V$		0.04		%/V
CE "High" Voltage	V_{CEH}	Start up	1.0			V
CE "Low" Voltage	V_{CEL}	Shut down			0.7	V
Output noise	EN	$I_{OUT} = 40mA, 300Hz \sim 50kHz$		50		μV_{rms}
Ripple Rejection Rate	PSRR	$V_{IN} = [V_{OUT} + 1]V + 1$	$I_{OUT} = 10mA, 1kHz$	70		dB
		V_{p-pAC}	$I_{OUT} = 100mA, 10kHz$	62		
			$I_{OUT} = 200mA, 10kHz$	62		
Short-circuit Current	I_{SHORT}	$V_{IN} = V_{OUT} + 1V, V_{CE} = V_{IN}, V_{OUT} = 0V$		120		mA

PT5108E23E-30
 $(V_{IN} = V_{OUT} + 1V, V_{CE} = V_{IN}, C_{IN} = C_L = 1\mu F, Ta = 25^\circ C)$, unless otherwise noted

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Output Voltage	$V_{OUT}(E)$ (Note 2)	$I_{OUT} = 30mA$, $V_{IN} = V_{OUT} + 1V$	X 0.98	$V_{OUT}(T)$ (Note 1)	X 1.02	V
Maximum Output Current	I_{OUTMAX}	$V_{IN} = V_{OUT} + 1V$		500		mA
Load Regulation	ΔV_{OUT}	$V_{IN} = V_{OUT} + 1V$, $1mA \leq I_{OUT} \leq 100mA$		8		mV
Dropout Voltage (Note 1)	V_{DIF1}	$I_{OUT} = 100mA$		100		mV
	V_{DIF2}	$I_{OUT} = 200mA$		210		mV
Supply Current	I_{SS}	$V_{IN} = V_{OUT} + 1V$		60		μA
Stand-by Current	I_{CEL}	$V_{CE} = 0V$		0		μA
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \cdot V_{OUT}}$	$I_{OUT} = 40mA$ $V_{OUT} + 1V \leq V_{IN} \leq 6.5V$		0.05		%/V
CE "High" Voltage	V_{CEH}	Start up	1.0			V
CE "Low" Voltage	V_{CEL}	Shut down			0.7	V
Output noise	EN	$I_{OUT} = 40mA$, 300Hz~50kHz		50		μV_{rms}
Ripple Rejection Rate	$PSRR$	$V_{IN} = [V_{OUT} + 1]V$	$I_{OUT} = 10mA, 1kHz$		70	dB
		+1Vp-pAC	$I_{OUT} = 100mA, 10kHz$		62	
			$I_{OUT} = 200mA, 10kHz$		62	
Short-circuit Current	I_{SHORT}	$V_{IN} = V_{OUT} + 1V$, $V_{CE} = V_{IN}$, $V_{OUT} = 0V$		120		mA

PT5108E23E-33
 $(V_{IN} = V_{OUT} + 1V, V_{CE} = V_{IN}, C_{IN} = C_L = 1\mu F, Ta = 25^\circ C)$, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Output Voltage	$V_{OUT}(E)$ (Note 2)	$I_{OUT} = 30mA$, $V_{IN} = V_{OUT} + 1V$	X 0.98	$V_{OUT}(T)$ (Note 1)	X 1.02	V
Maximum Output Current	I_{OUTMAX}	$V_{IN} = V_{OUT} + 1V$		500		mA
Load Regulation	ΔV_{OUT}	$V_{IN} = V_{OUT} + 1V$, $1mA \leq I_{OUT} \leq 100mA$		9		mV
Dropout Voltage (Note 1)	V_{DIF1}	$I_{OUT} = 100mA$		120		mV
	V_{DIF2}	$I_{OUT} = 200mA$		260		mV
Supply Current	I_{SS}	$V_{IN} = V_{OUT} + 1V$		55		μA
Stand-by Current	I_{CEL}	$V_{CE} = 0V$		0.1		μA
Line Regulation	ΔV_{OUT} $\Delta V_{IN} \cdot V_{OUT}$	$I_{OUT} = 40mA$ $V_{OUT} + 1V \leq V_{IN} \leq 6.5V$		0.05		%/V
CE "High" Voltage	V_{CEH}	Start up	1.0			V
CE "Low" Voltage	V_{CEL}	Shut down			0.7	V
Output noise	EN	$I_{OUT} = 40mA$, 300Hz~50kHz		50		μV_{rms}
Ripple Rejection Rate	PSRR	$V_{IN} = [V_{OUT} + 1]V + 1V_{p-pAC}$	$I_{OUT} = 10mA, 1kHz$ Z	70		dB
			$I_{OUT} = 100mA, 10kHz$ Z	62		
			$I_{OUT} = 200mA, 10kHz$ Z	62		
Short-circuit Current	I_{SHORT}	$V_{IN} = V_{OUT} + 1V, V_{CE} = V_{IN}, V_{OUT} = 0V$		150		mA

Note :

1. $V_{OUT}(T)$: Specified Output Voltage

2. $V_{OUT}(E)$: Effective Output Voltage (i.e. The output voltage when " $V_{OUT}(T) + 1.0V$ " is provided at the Vin pin while maintaining a certain I_{OUT} value.)

3. V_{DIF} : $V_{IN1} - V_{OUT}(E)'$

V_{IN1} : The input voltage when $V_{OUT}(E)'$ appears as input voltage is gradually decreased.

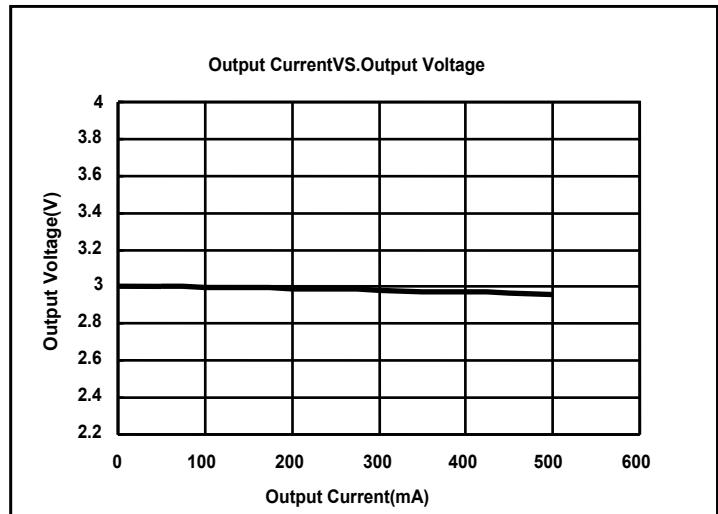
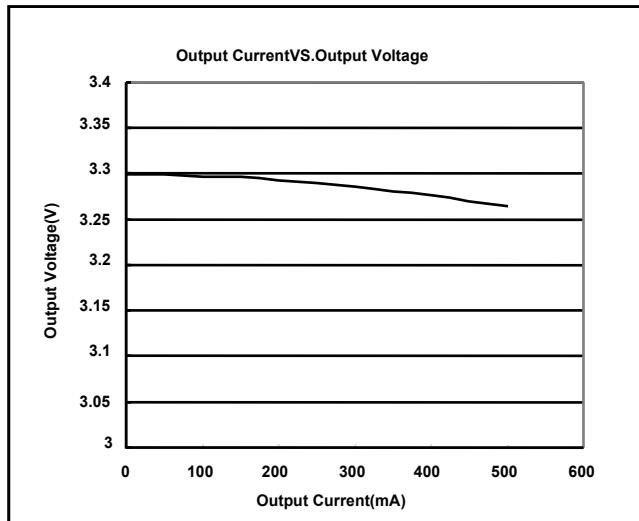
$V_{OUT}(E)'$ =A voltage equal to 98% of the output voltage whenever an amply stabilized I_{OUT} { $V_{OUT}(T) + 1.0V$ } is input.

Type Characteristics

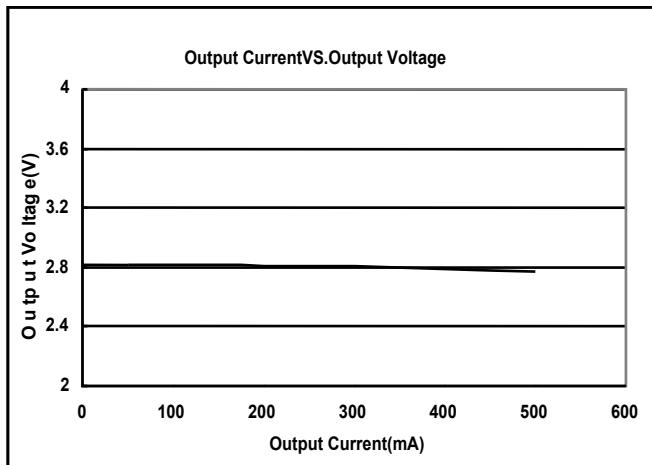
 (1) Output CurrentVS.Output Voltage ($V_{IN}=V_{out}+1$, $T_a = 25^{\circ}\text{C}$)

PT5108E23E-33

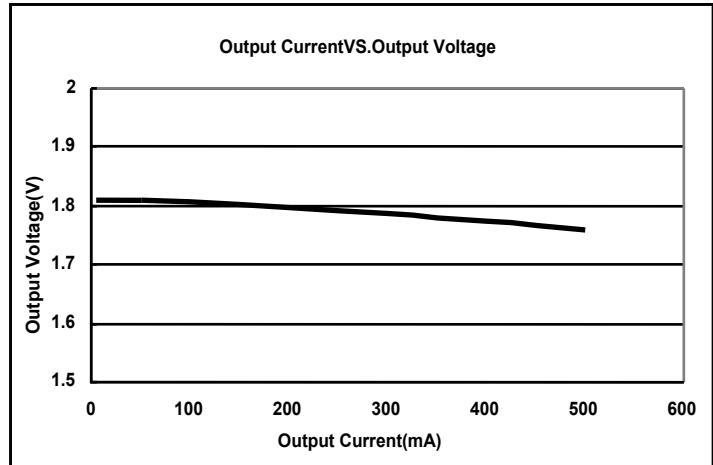
PT5108E23E-30



PT5108E23E-28



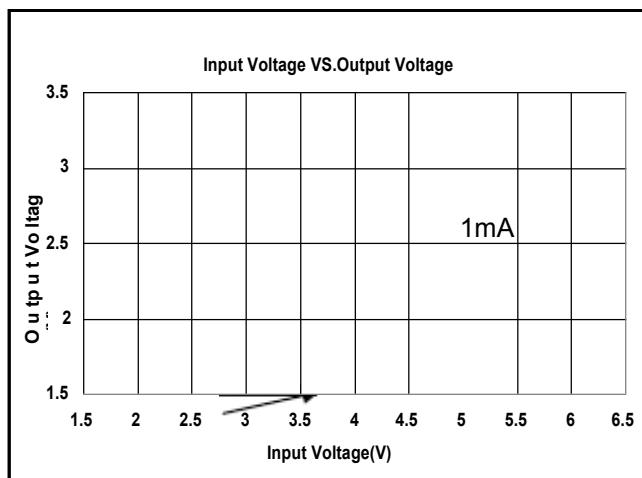
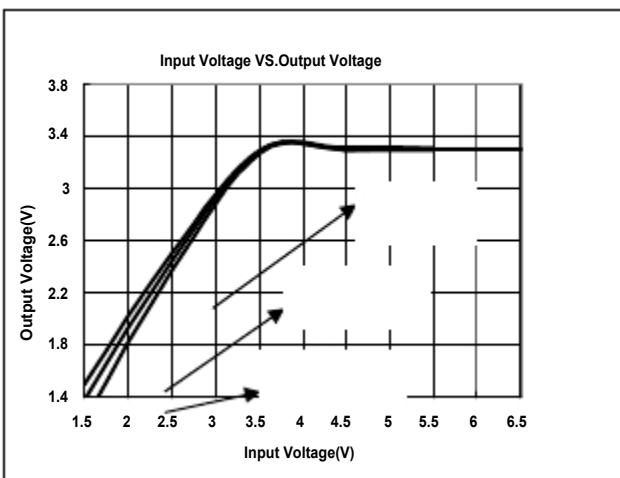
PT5108X23E-18



(2) Input Voltage VS. Output Voltage ($T_a = 25^\circ C$)

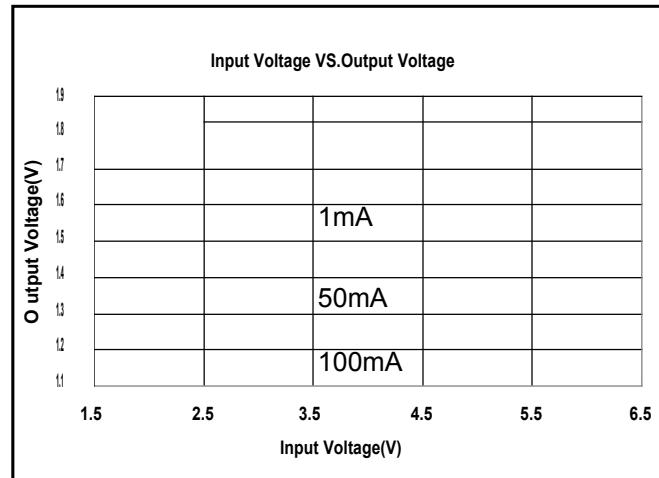
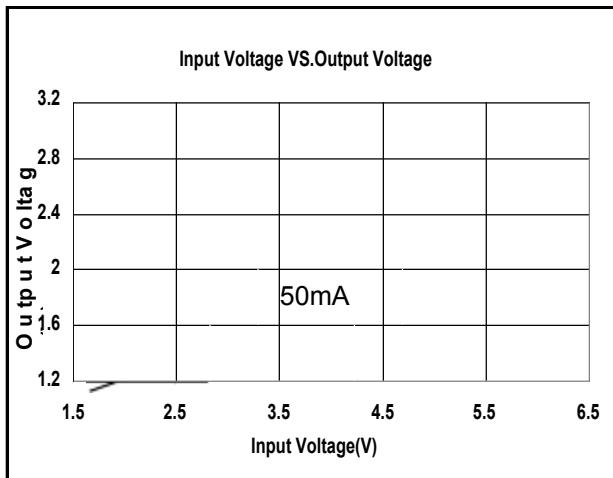
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PT5108E23E-30



PT5108E23E-28

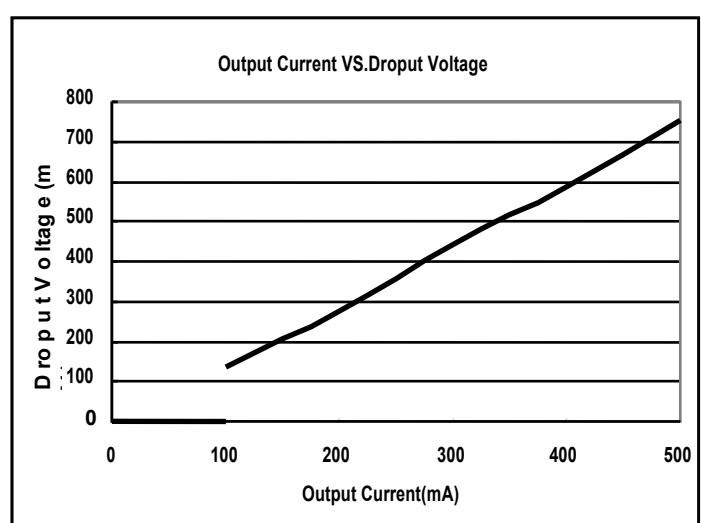
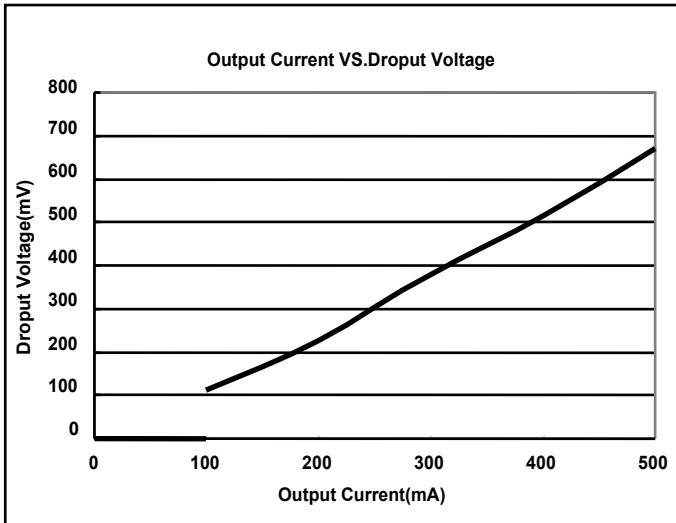
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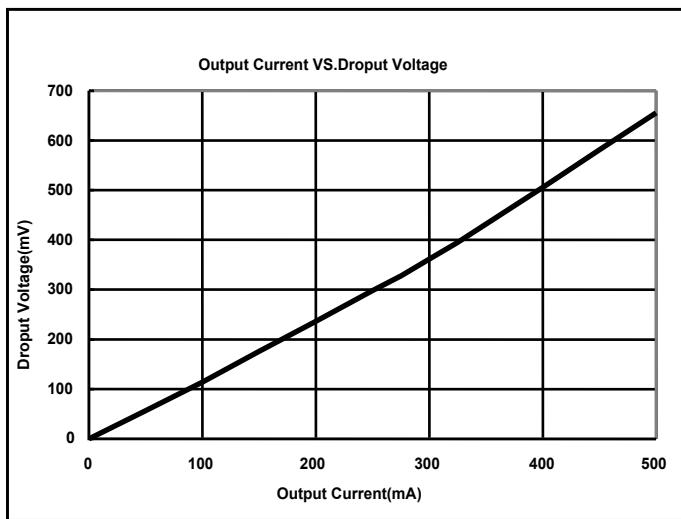
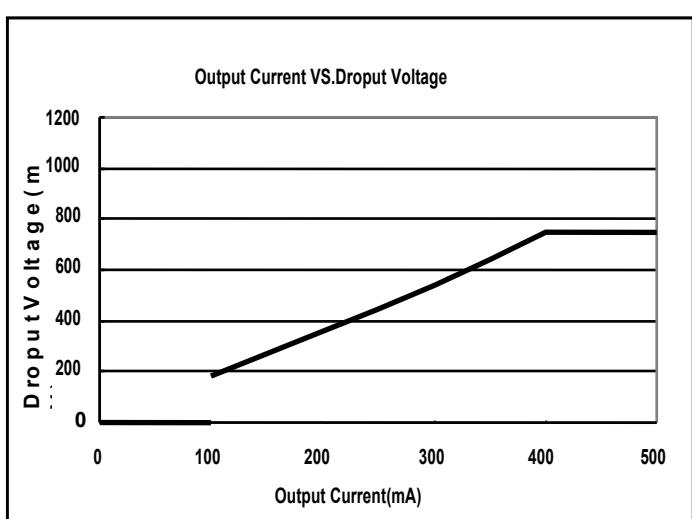


(3) Output Current VS. Dropout Voltage ($V_{IN}=V_{out}+1V$, $T_a = 25^\circ C$)

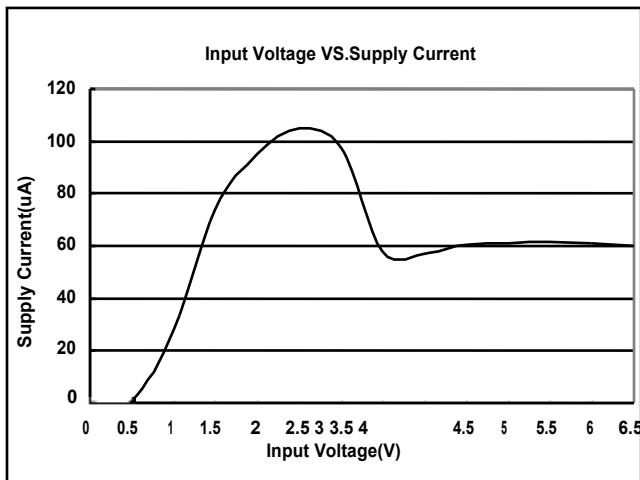
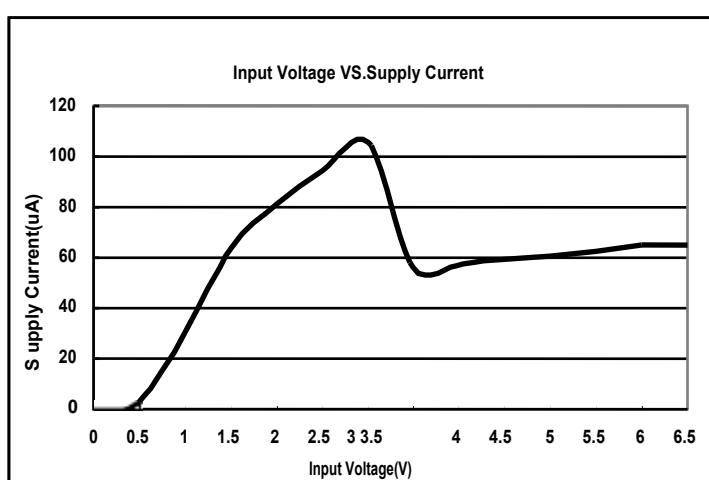
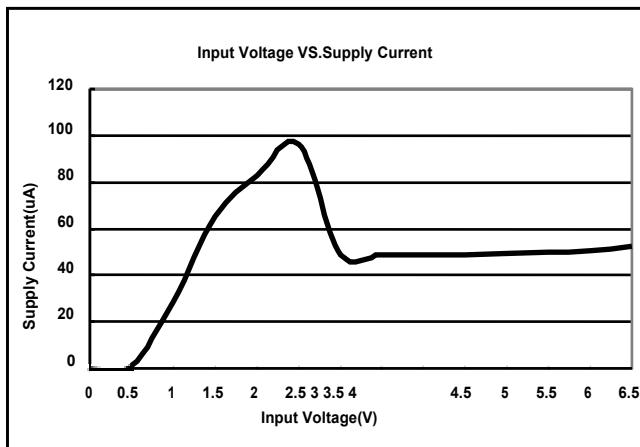
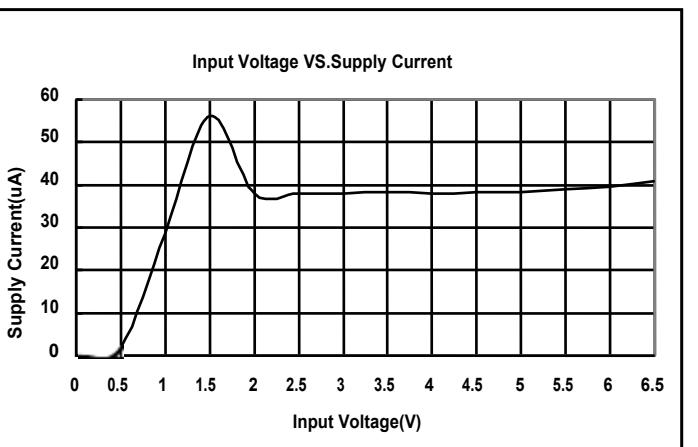
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PT5108E23E-30



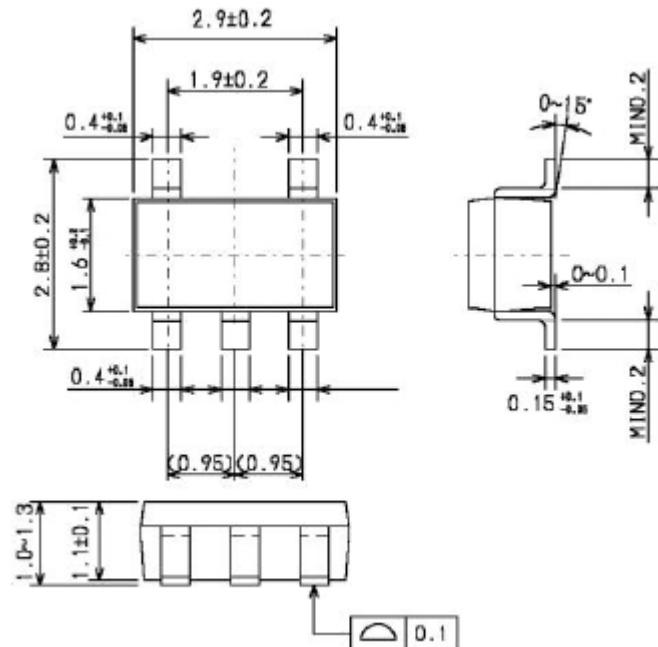
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PT5108X23E-18


(4) Input Voltage VS. Supply Current ($T_a = 25^\circ C$)

PT5108E23E-33

PT5108E23E-30

PT5108E23E-28

PT5108X23E-18


Packaging Information:

- SOT23-5


ORDERING INFORMATION

Package	Temperature Range	Output Voltage(V)	Ordering Part Number	Mark
SOT23-5	-40°C to 85°C	1.2V	PT5108E23E-12	S4xx
		1.8V	PT5108E23E-18	5108I S5xx
		2.5V	PT5108E23E-25	S6xx
		2.8V	PT5108E23E-28	S1xx
		3.0V	PT5108E23E-30	G2xx S3xx
		3.3V	PT5108E23E-33	5108D

ORDERING INFORMATION

PART NUMBER	PACKAGE	TAPE&REEL
PT5108E23E	SOT23-5	3000PCS&REEL