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Title: Index

File: FR_RK3288

REV: V00

Create Date: Sunday, January 26, 2014

Page Num: 1

Modify Date: Thursday, December 10, 2015

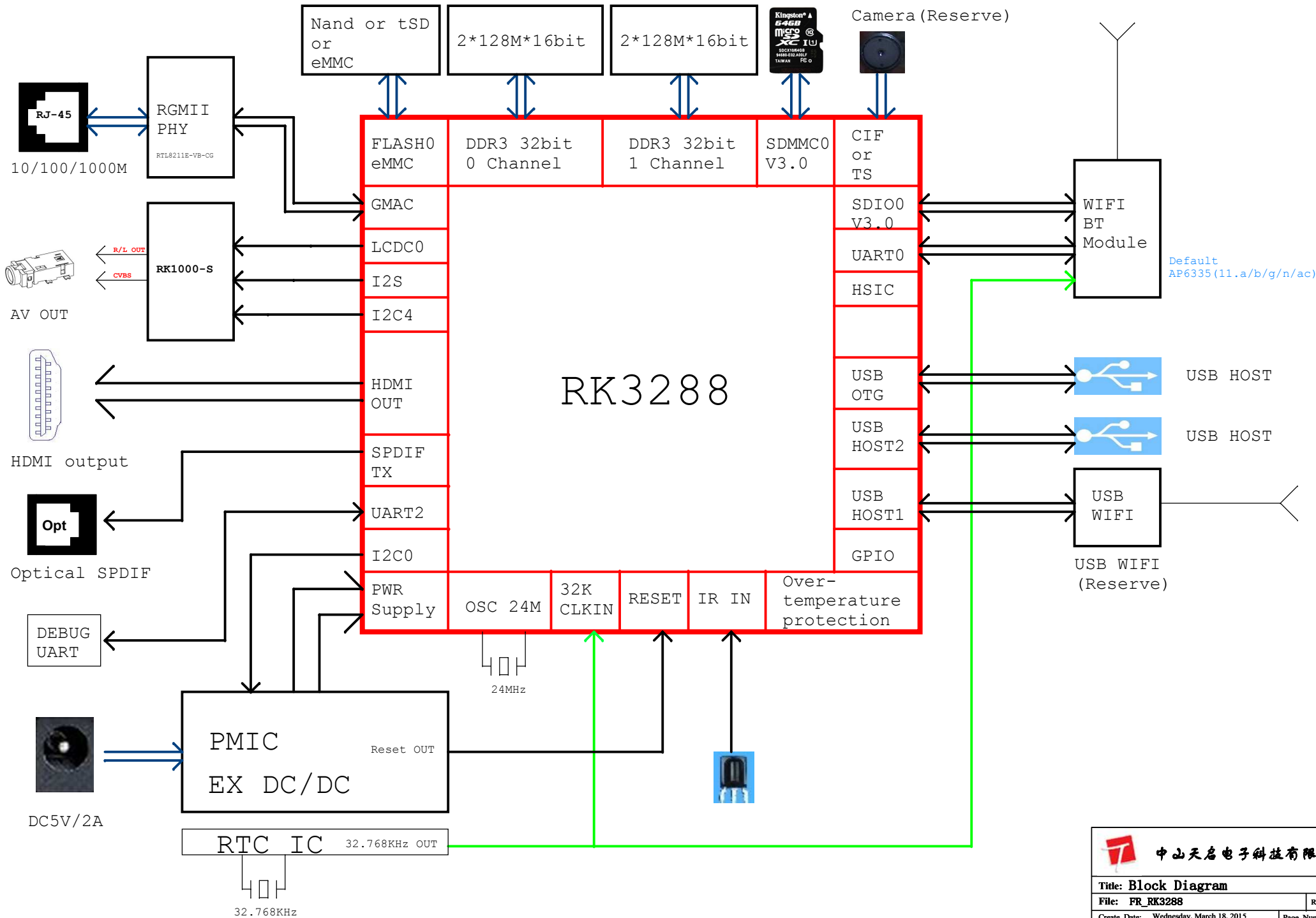
Page Total: 18

Version	Date	Author	Change Note	Approved
V00	20150318	HWQ	First edictor	

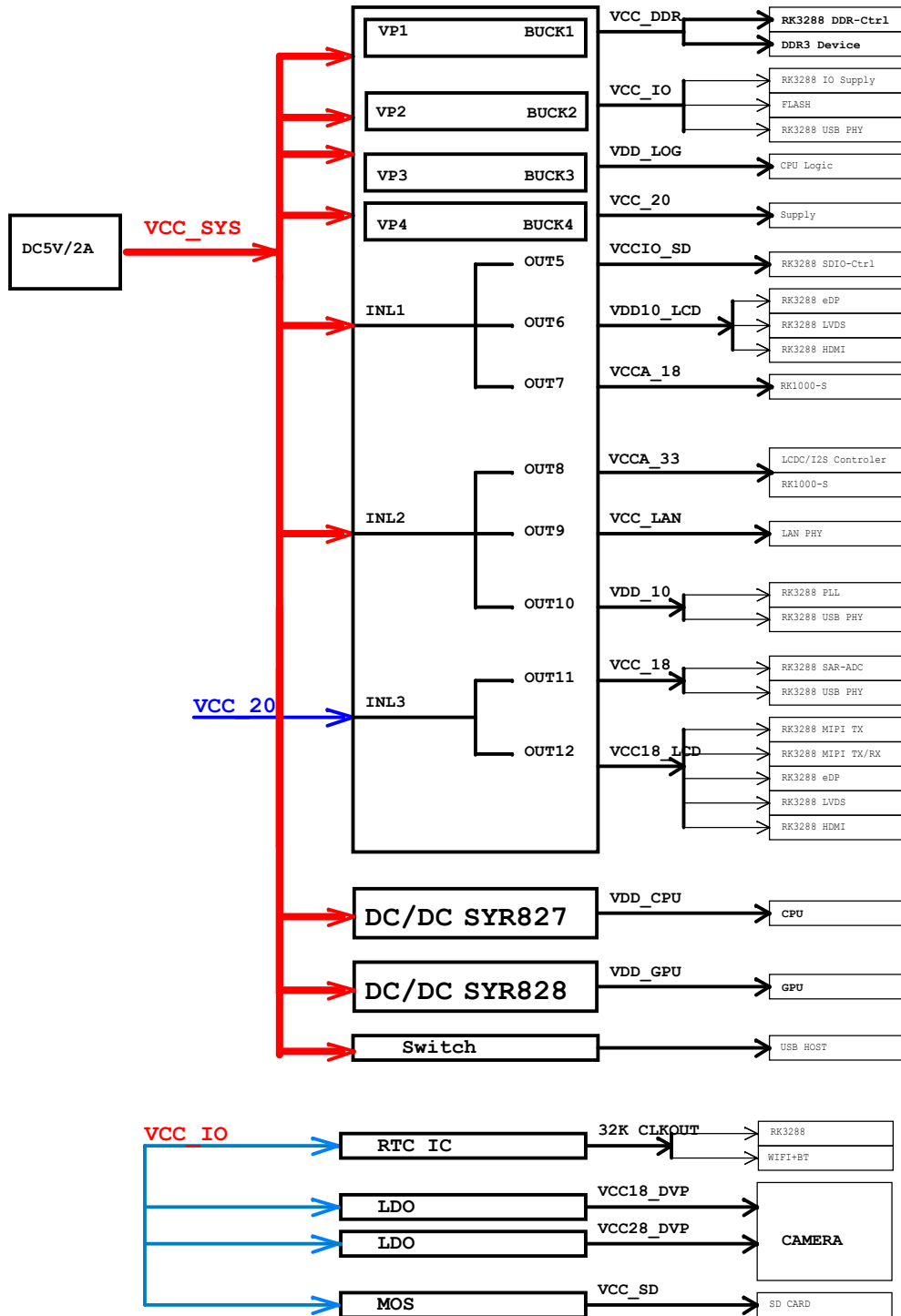


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Title: Modify note	
File: FR_RK3288	REV: V00
Create Date: Saturday, October 23, 2010	Page Num: 2
Modify Date: Thursday, December 10, 2015	Page Total: 18



ACT8846QM489



PowerName	PMIC Channel	timer (2mS)	Default voltage	Working voltage
VCC_20	DCDC4	Solt:0	2.0V	2.0V
VDD_10	OUT10	Solt:1	1.0V	1.0V
VCC_DDR	DCDC1	Solt:2	1.5V (FB=1.2V)	1.5V (DDR3)
VDD_CPU	EX DCDC	Solt:2A	1.0V	DVFS
VDD_GPU	EX DCDC	Solt:2B	1.0V	DVFS
VDD_LOG	DCDC3	Solt:3	1.1V	DVFS
VCC_18	OUT11	Solt:4	1.8V	1.8V
VCC_LAN	OUT9	Solt:5	3.3V	3.3V
VCC_IO	DCDC2	Solt:6	3.3V	3.3V
VCCIO_SD	OUT5	Solt:6	3.3V	SD2.0:3.3V SD3.0:1.8V
VCCA_33	OUT8	OFF	OFF	3.3V
VCCA_18	OUT7	OFF	OFF	1.8V
VDD10_LCD	OUT6	OFF	OFF	1.0V
VCC18_LCD	OUT12	OFF	OFF	1.8V



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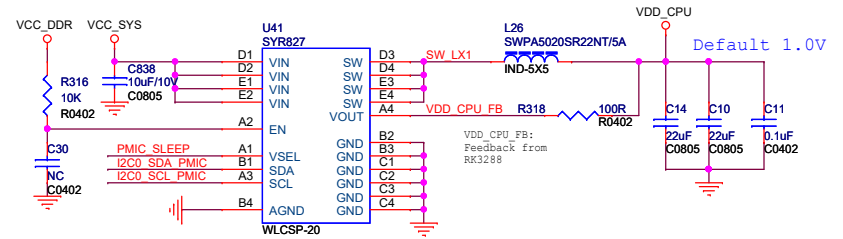
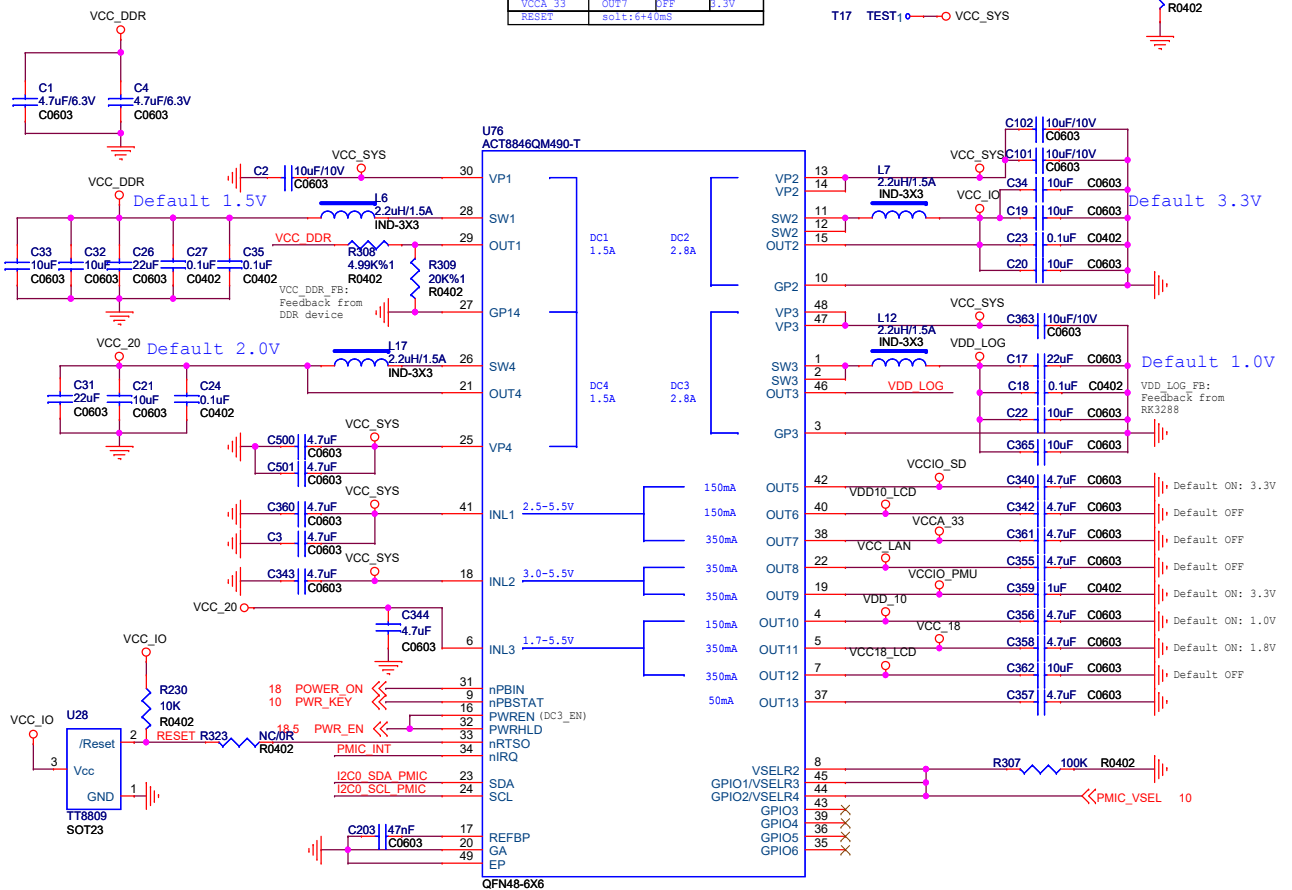
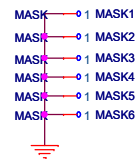
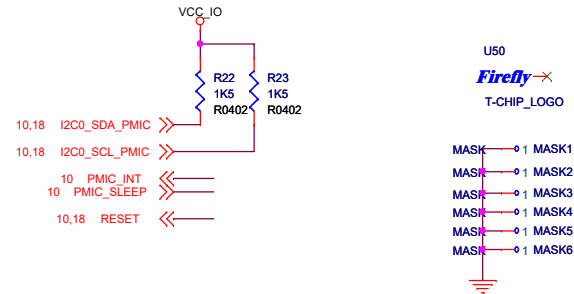
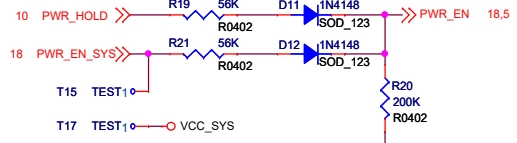
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File: FR_RK3288 REV: V00

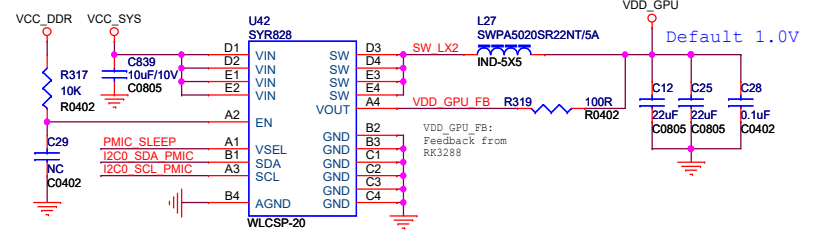
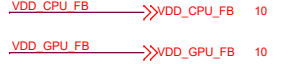
Create Date: Sunday, January 26, 2014	Page Num: 4
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SYSTEM POWER

PowerName	PMU Channel	TIMER (1mS)	OutPut voltage
VCC_IO	DCDC2	01t:6	1.3V
VCC_20	DCDC4	01t:0	2.0V
VCCIO_LCD	EXMos	0FF	1.3V
VCC_LOG	DCDC3	01t:3	1.0V
VCC_DDR	DCDC1	01t:2	1.2V
VDD_ARM	EXDCDC1	01t:2A	1.0V
VDD_GPU	EXDCDC2	01t:2B	1.0V
VCC_LAN	OUT8	0FF	1.3V
VCC_SD	EXMos	01t:6A	1.3V
VCCIO_WL	EXMos	0FF	1.8V
VDDIO_LCD	OUT6	0FF	1.0V
VCCIO_SD	OUT5	01t:6	1.3V
VCC18_LCD	OUT12	0FF	1.8V
VDD_10	OUT10	01t:1	1.0V
VCC_18	OUT11	01t:4	1.8V
VCCIO_PMU	OUT9	01t:5	1.3V
VCCA_33	OUT7	0FF	1.3V
RESBT	01t:6+40ms		



SYR827	I2C Address: 0x40h
SYR828	I2C Address: 0x41h



If you use the MCU_J18506 - double code - V6.04, need to use external reset IC, R323 don't paste.

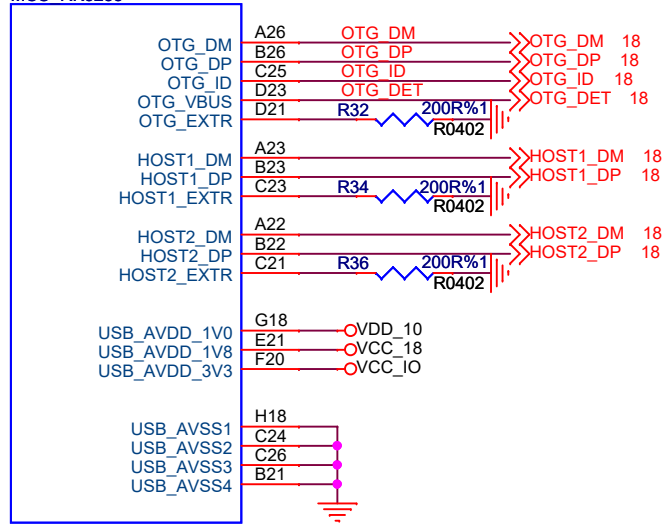
中山天启电子科技有限公司

Title: System Power

File: FR_RK3288 REV: V00

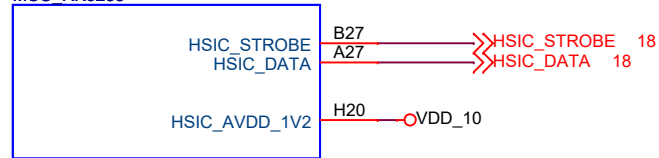
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Modify Date: Wednesday, December 23, 2015 Page Total: 18

U1E
MCU RK3288

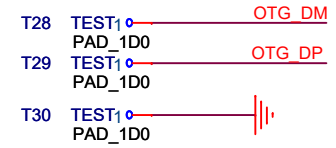
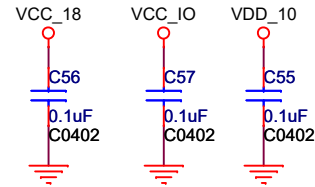
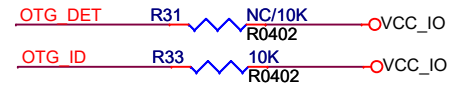


RK3288_E

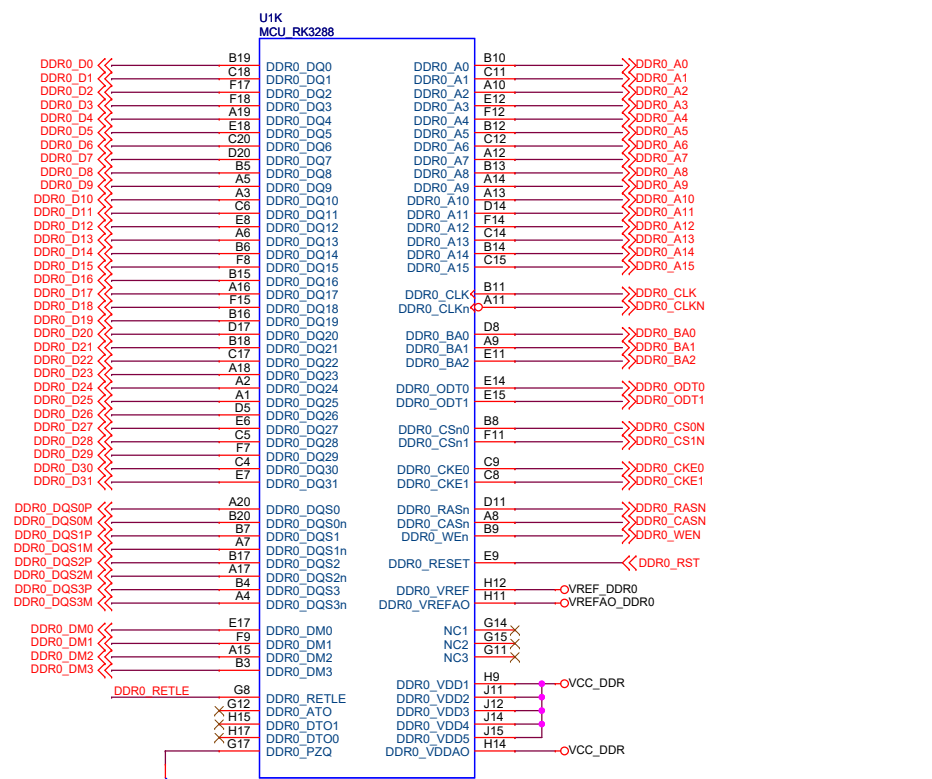
U1U
MCU RK3288



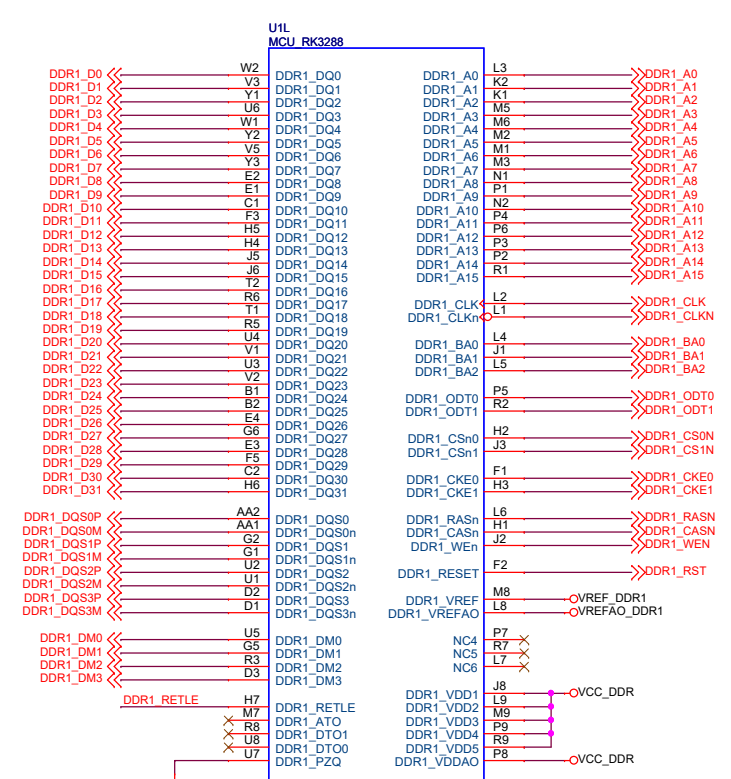
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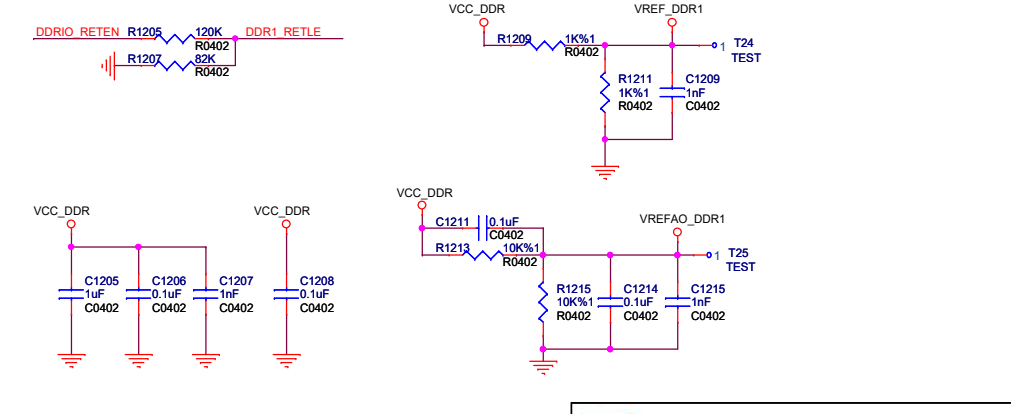
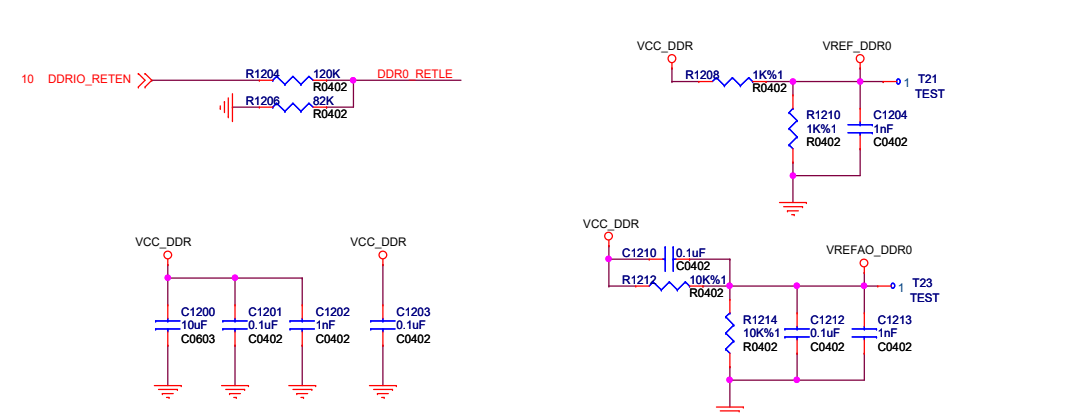
 中山天启电子科技有限公司	
Title: RK3288 USB/HSIC Controller	
File: FR_RK3288	REV: V00
Create Date: Monday, May 12, 2014	Page Num: 6
Modify Date: Thursday, December 10, 2015	Page Total: 18



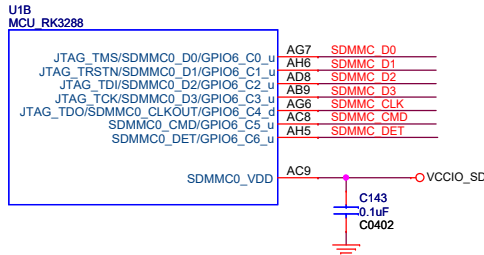
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Channel 0



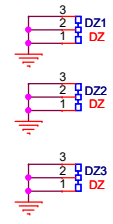
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Channel 1



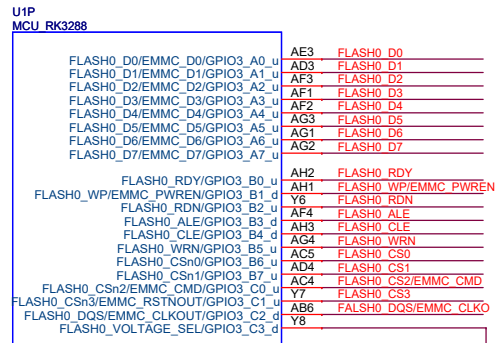
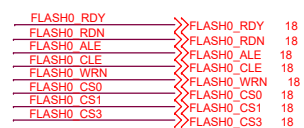
TF Card



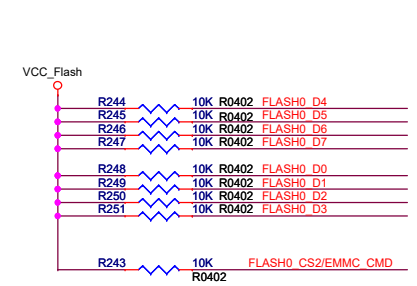
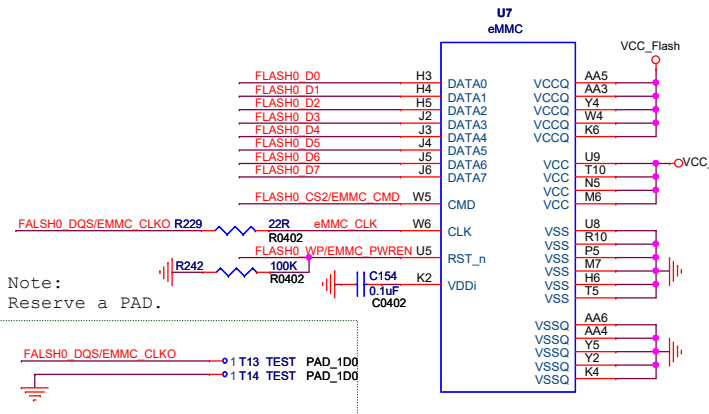
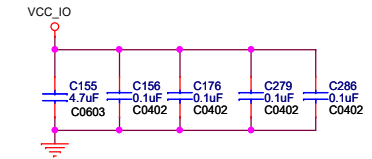
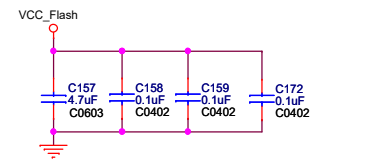
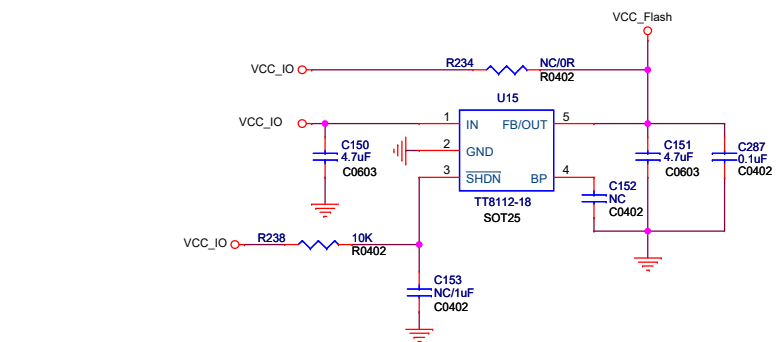
RK3288_B

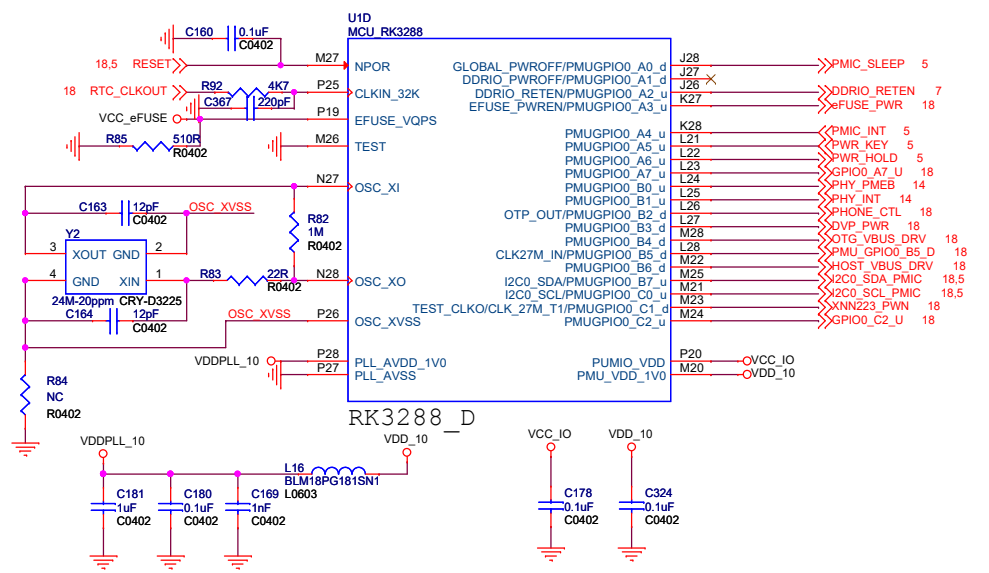


eMMC

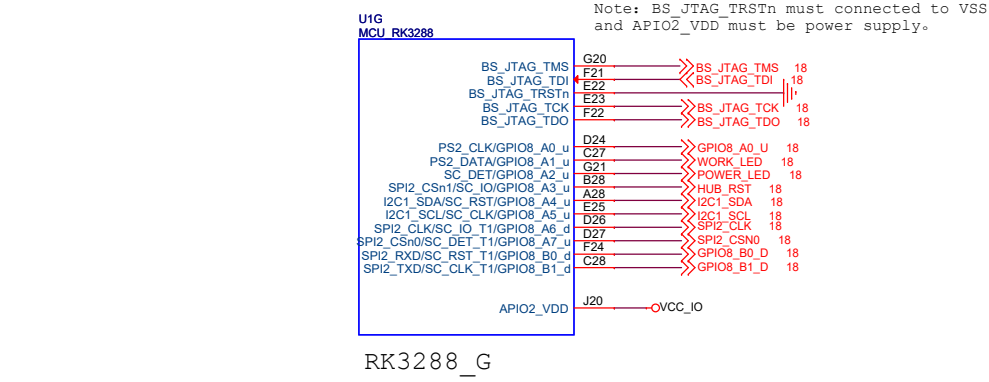


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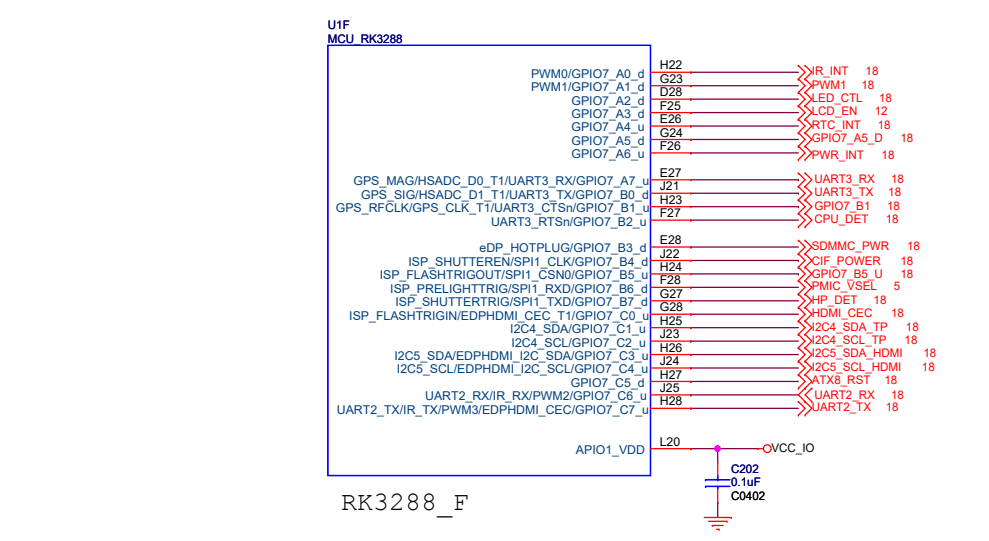




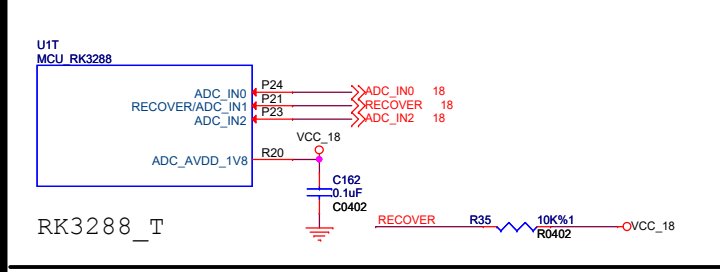
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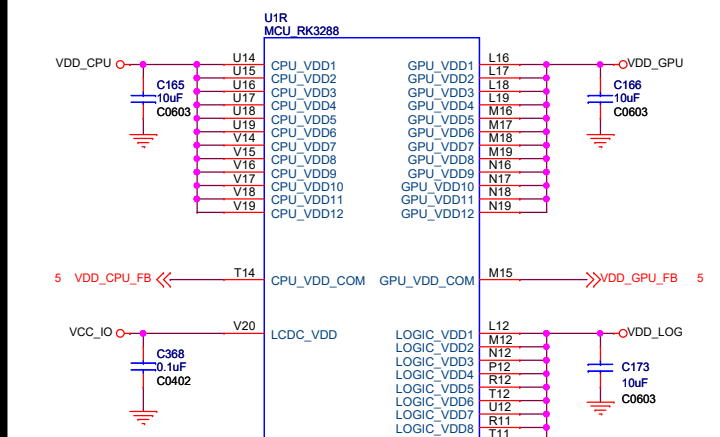
RK3288_G



RK3288_F

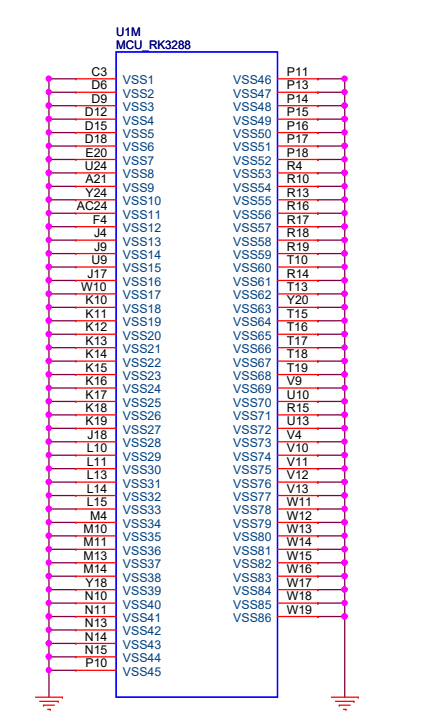
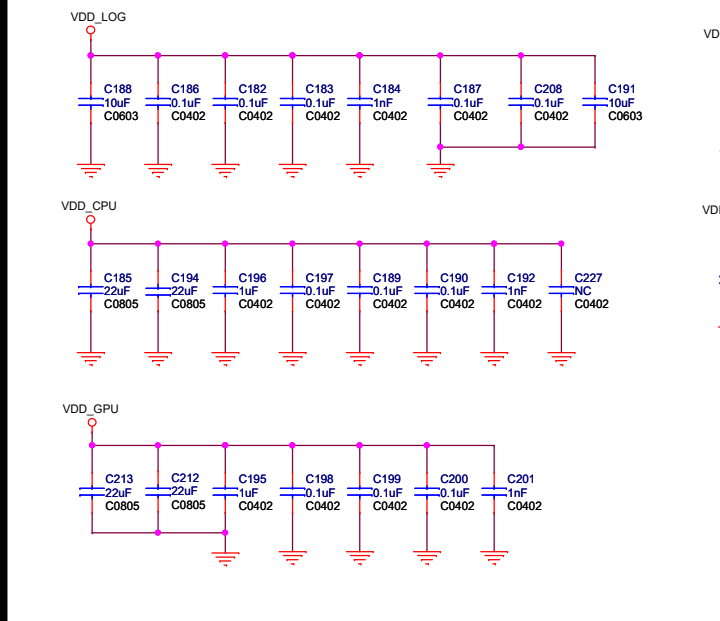


RK3288_T



RK3288_R

RK3288 FILTER

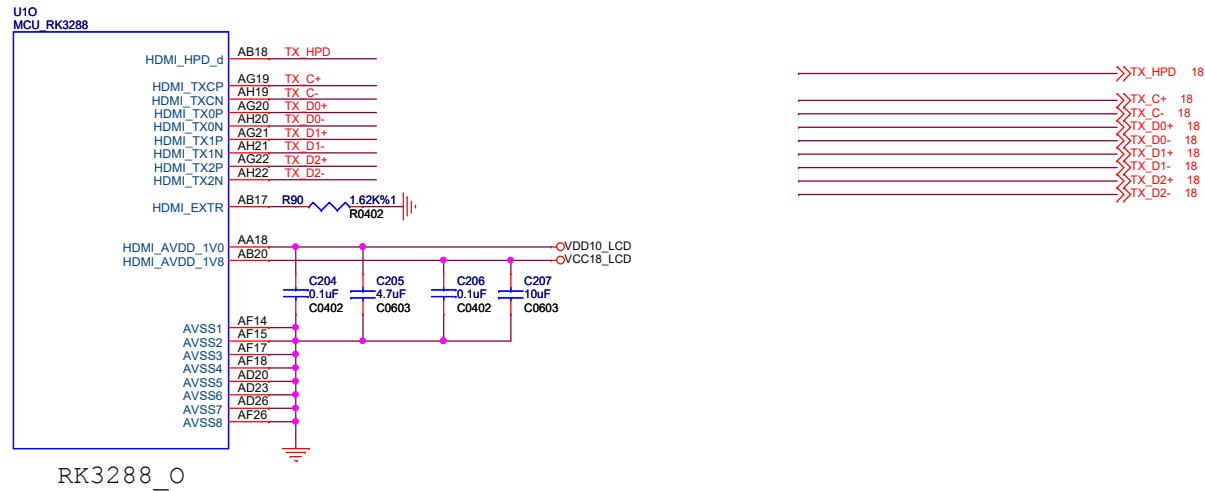


RK3288_M

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Title: RK3288 GPIO/POWER
 File: FR_RK3288
 Create Date: Monday, May 12, 2014
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 REV: V00

HDMI_OUT

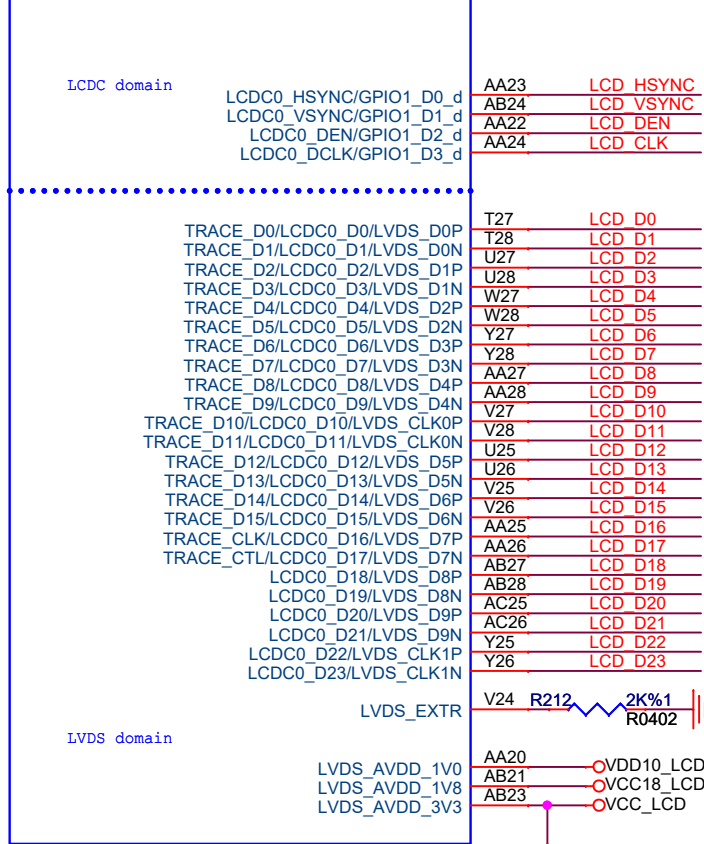


RK3288_O

RK3288_O

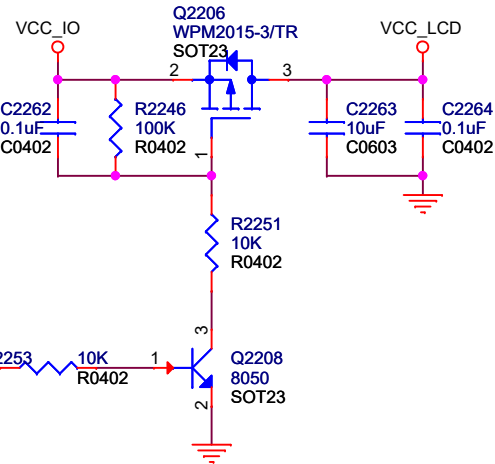


U1A
MCU RK3288

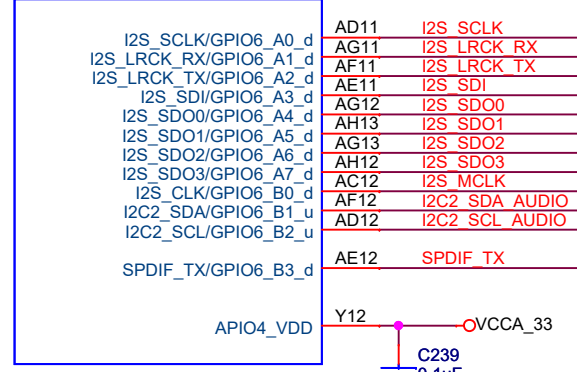


RK3288_A

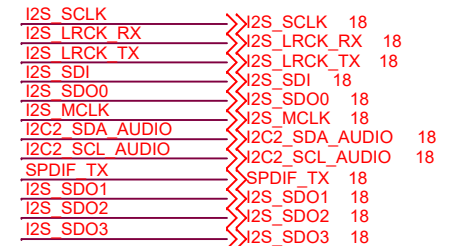
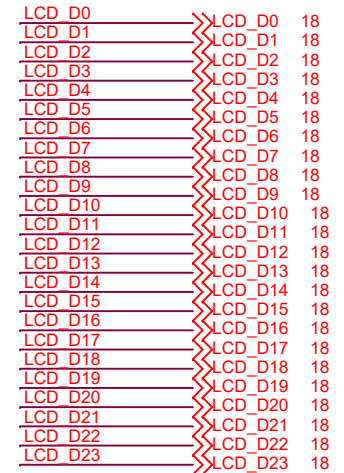
Note: Please hold the connection in sequence and use LVDS_D0~D4 for single LVDS.



U1I
MCU RK3288



RK3288_I



中山天启电子科技有限公司

Title: RK3288 LCDC/I2S Controller

File: FR_RK3288

REV: V00

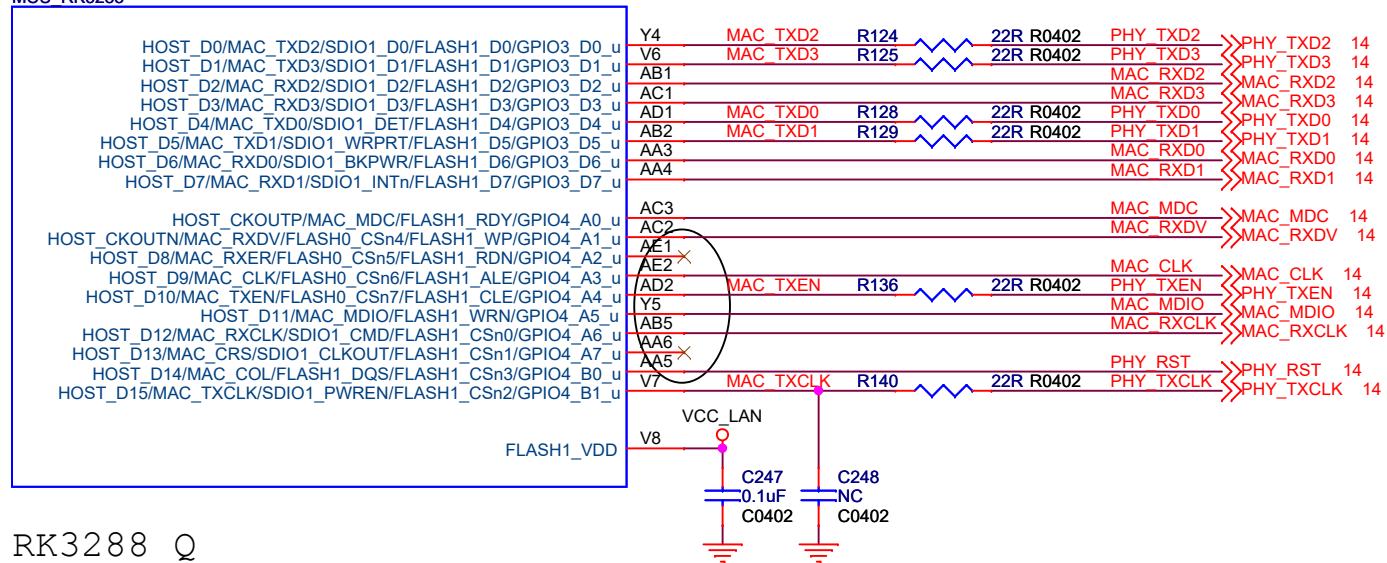
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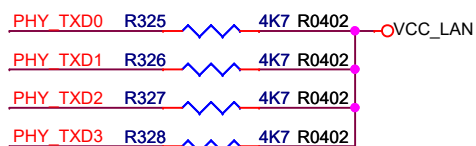
Page Num: 12


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U1Q
MCU_RK3288



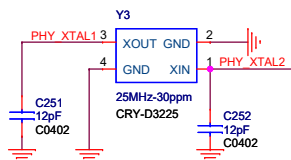
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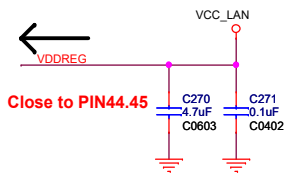
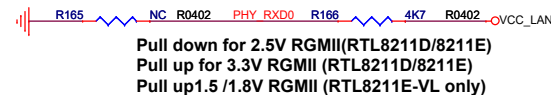
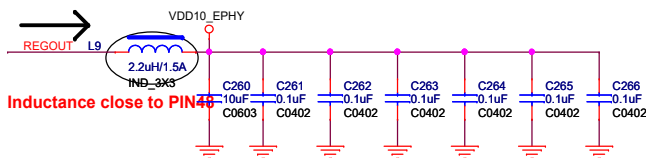
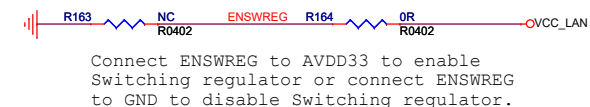
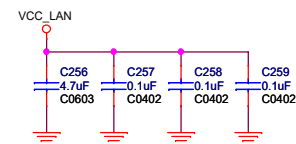
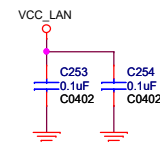
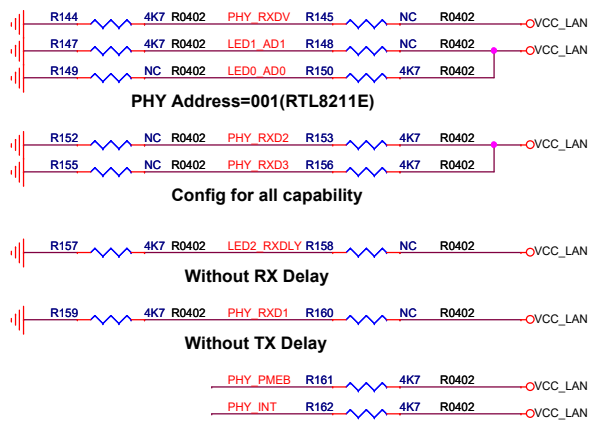
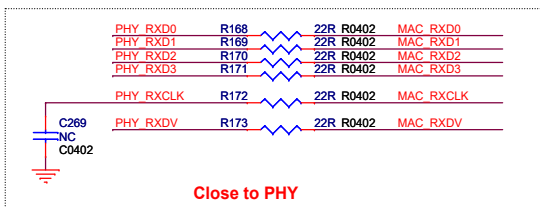
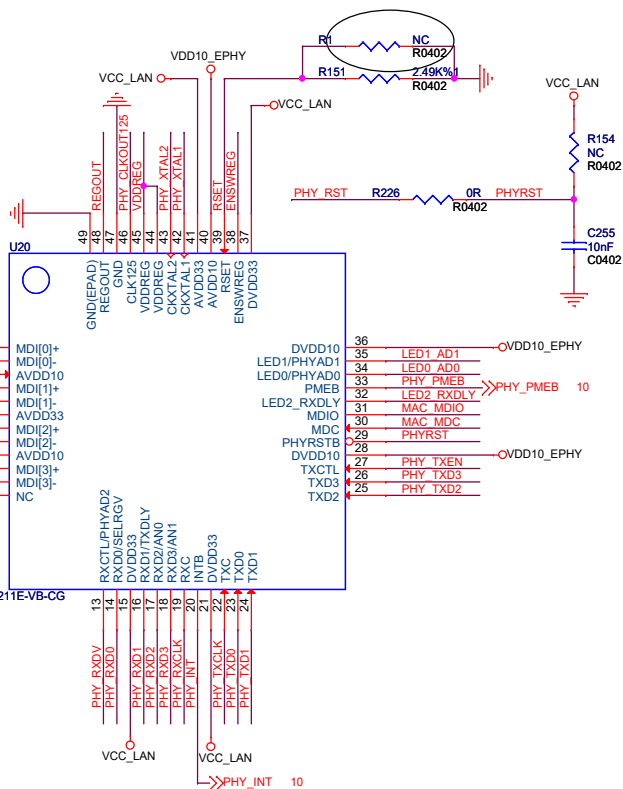
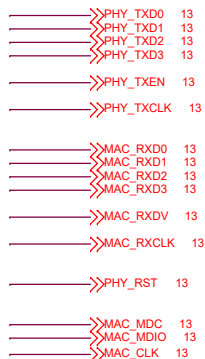
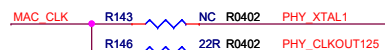
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Title: RK3288 EthernetMAC Controller
 File: FR_RK3288 REV: V00
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10/100/1000M-PHY



If use external clock then the XTAL2 need connect to GND for RTL8211E.



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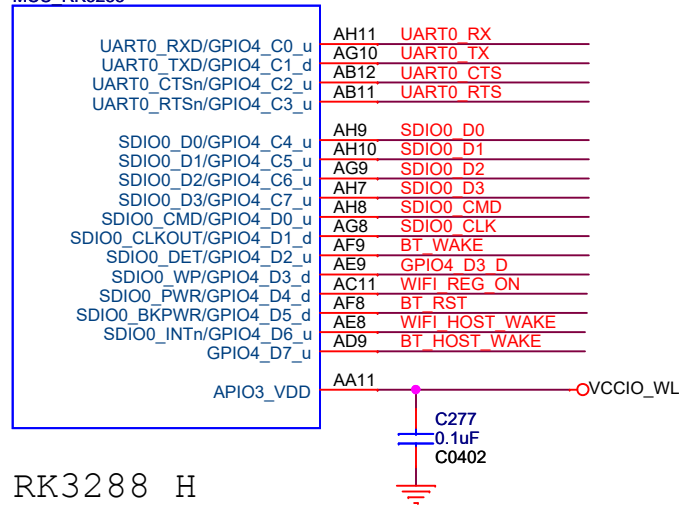
Title: 10/100/1000M-PHY

File: FR_RK3288 REV: V00

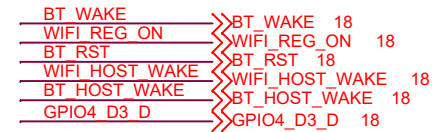
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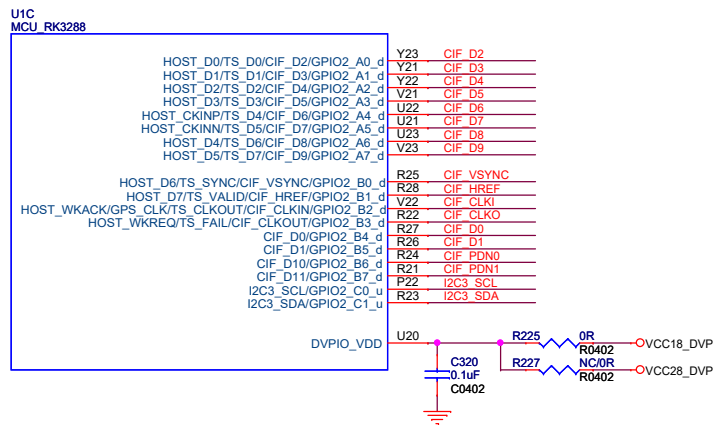
U1H
MCU RK3288



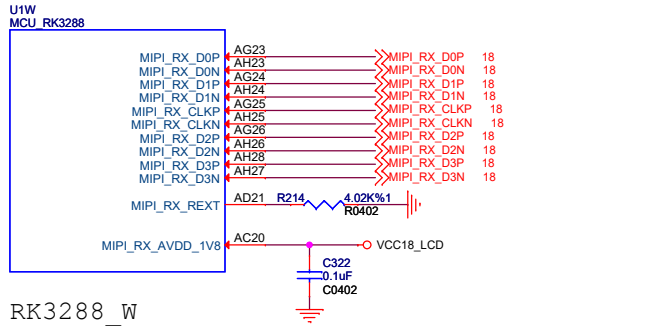
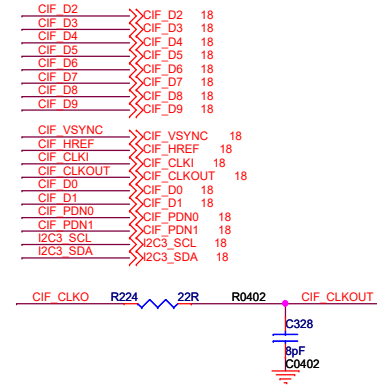
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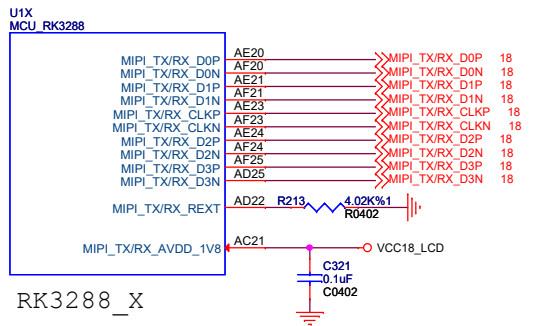
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File: FR_RK3288	REV: V00
Create Date: Monday, May 12, 2014	Page Num: 15
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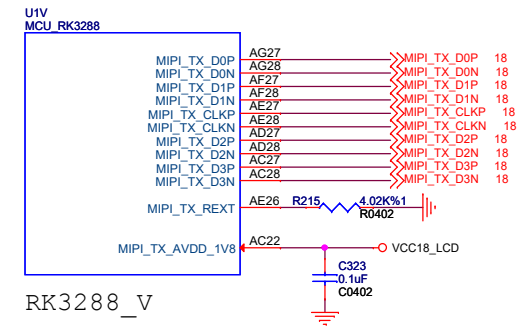
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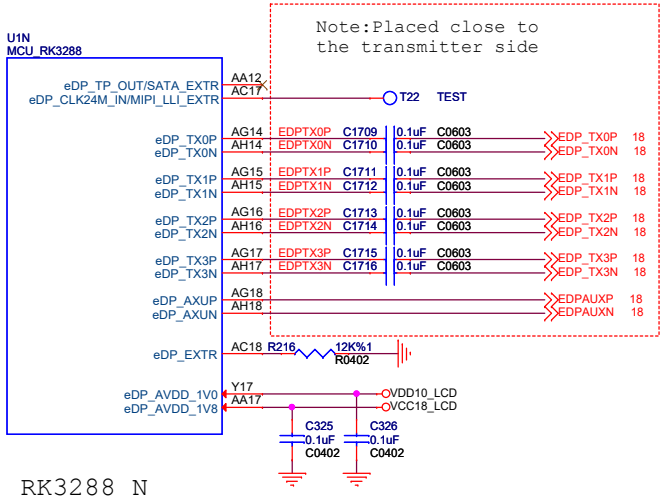
RK3288_W



RK3288_X



RK3288_V



RK3288_N

中山天启电子科技有限公司

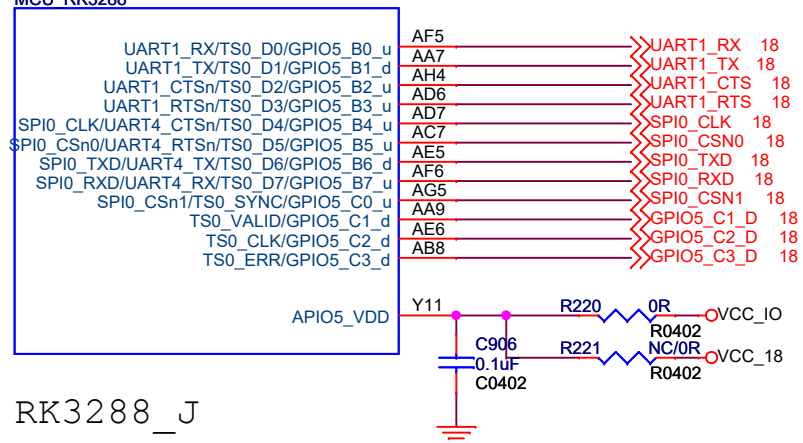
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File: FR_RK3288 REV: V00


Create Date: Monday, May 12, 2014 Page Num: 16

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U1J
MCU RK3288



RK3288_J

 中山天启电子科技有限公司	
Title: DTV /SPI Interface	
File: FR_RK3288	REV: V00
Create Date: Monday, May 12, 2014	Page Num: 17
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